

FIG. 1
PRIOR ART

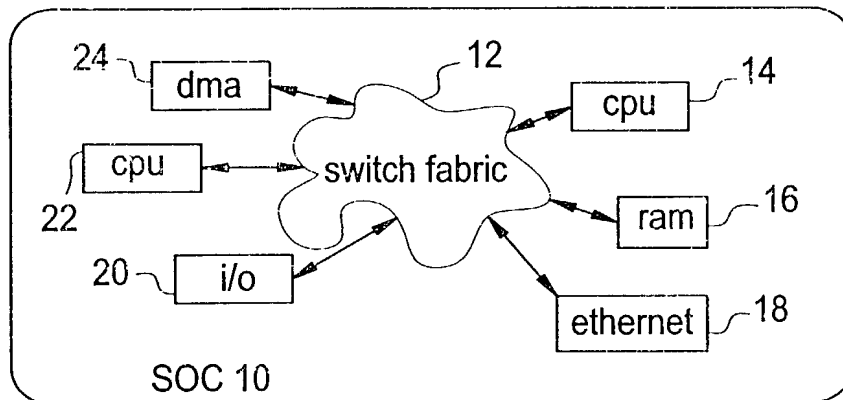
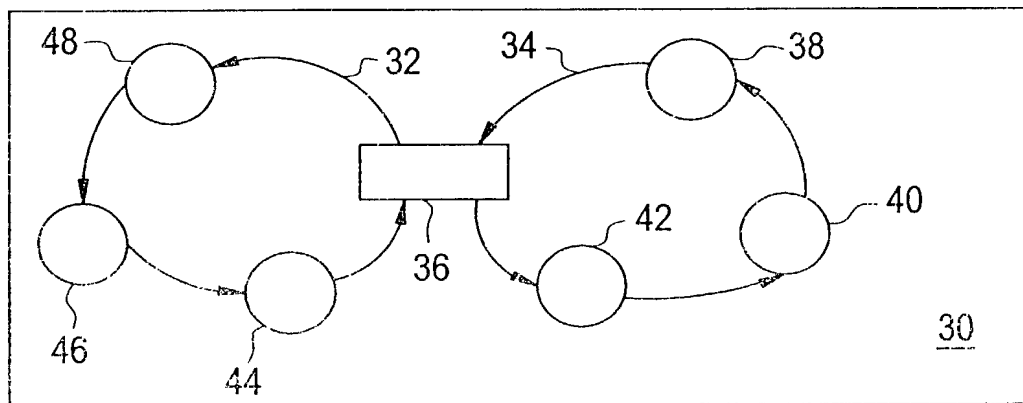


FIG. 2



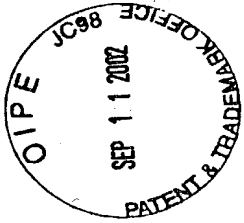


FIG. 3

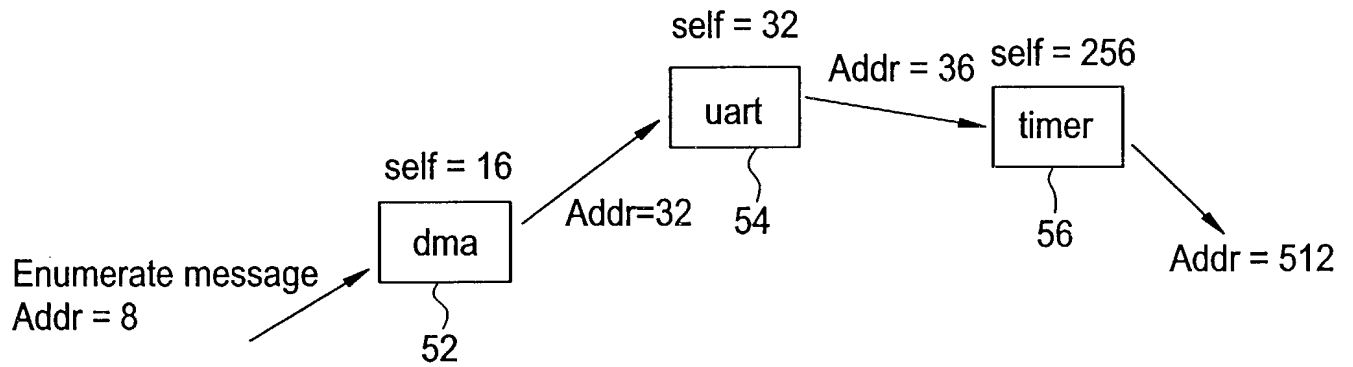


FIG. 4

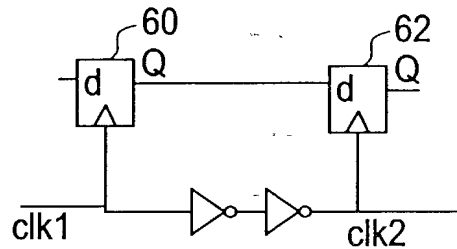
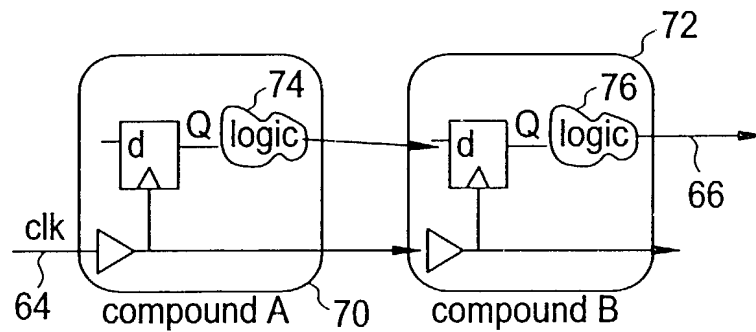


FIG. 5



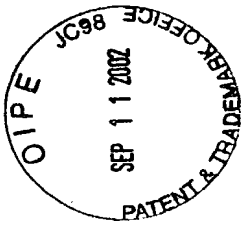


FIG. 6

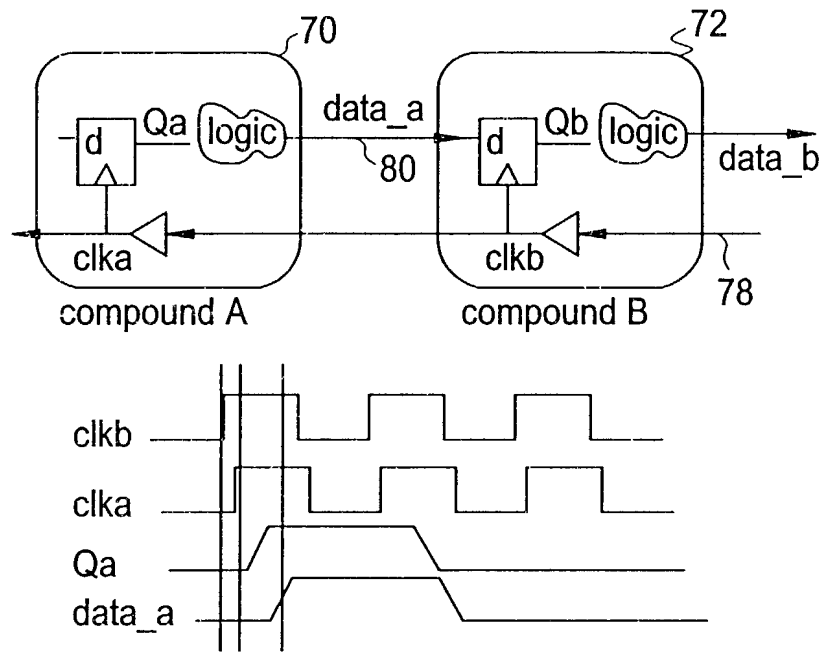


FIG. 7

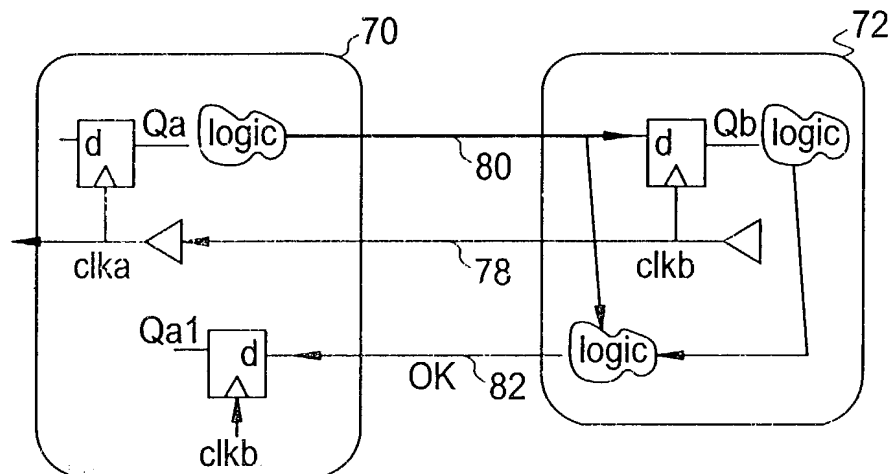


FIG. 8

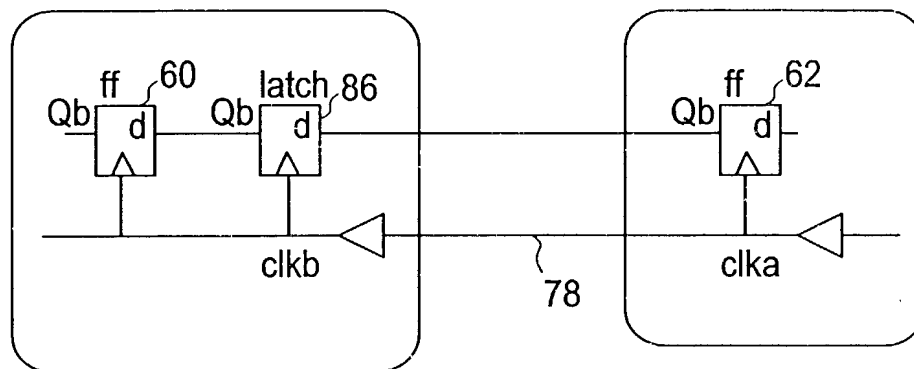


FIG. 9

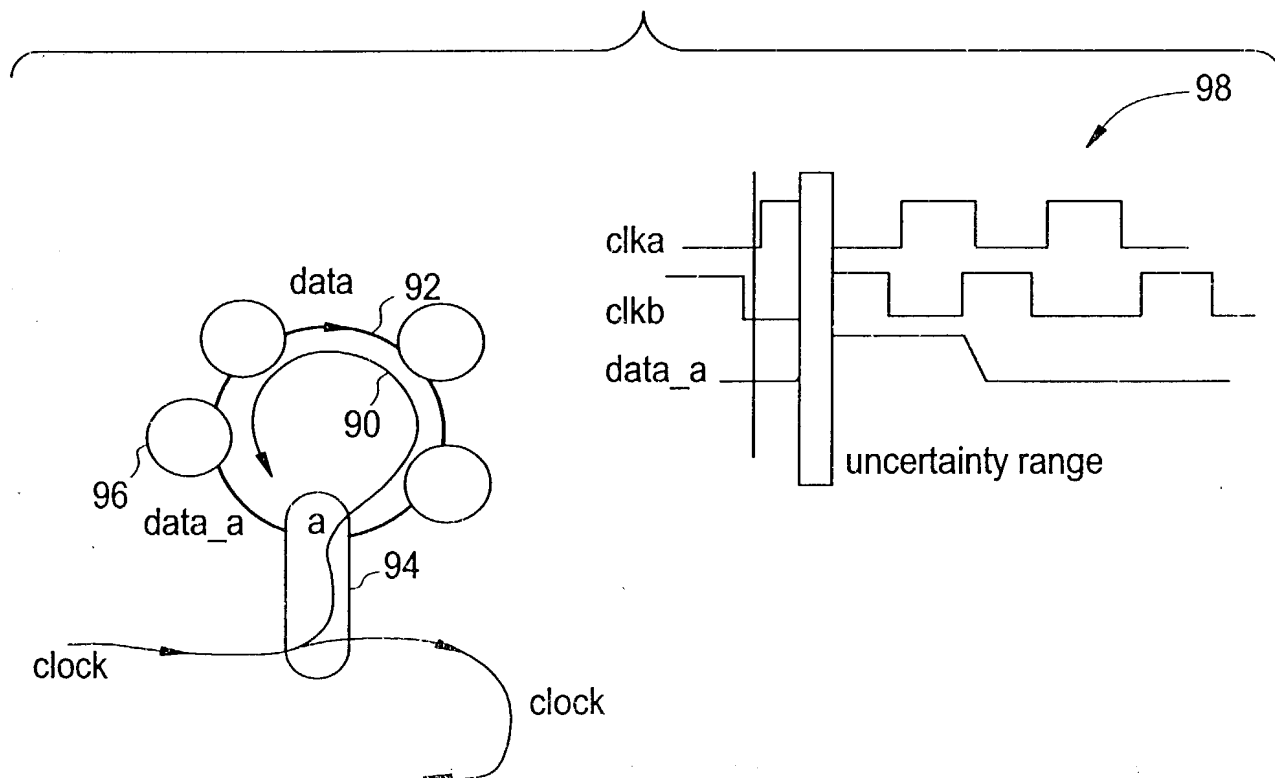


FIG. 10

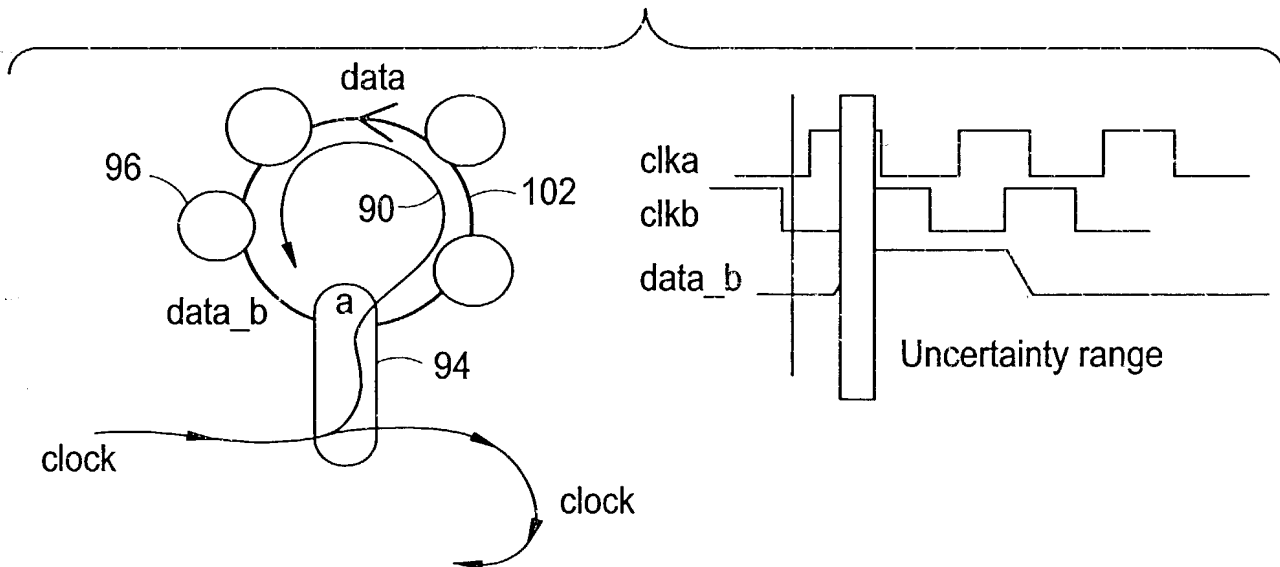


FIG. 11

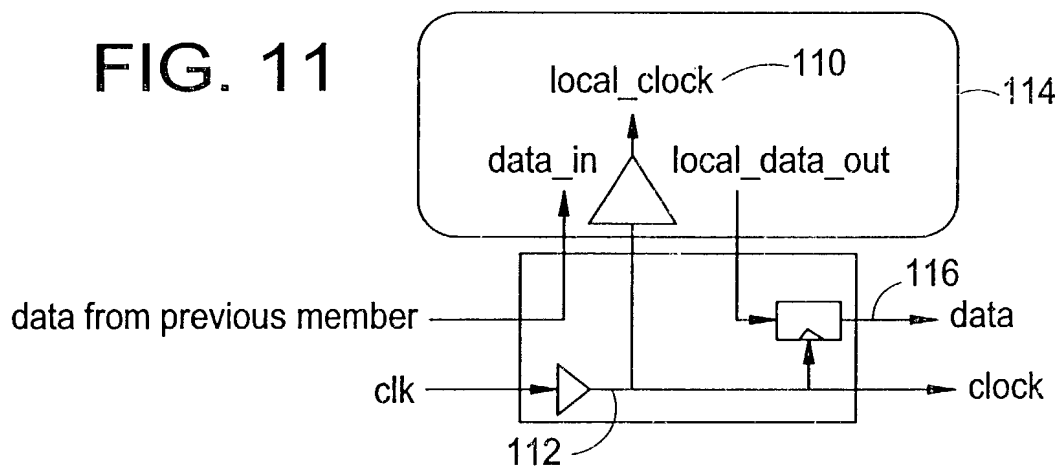


FIG. 12

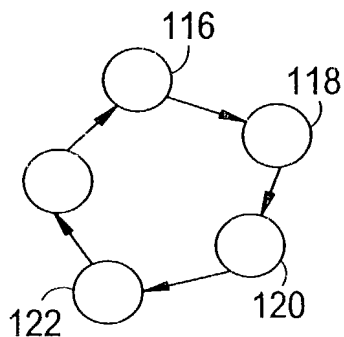
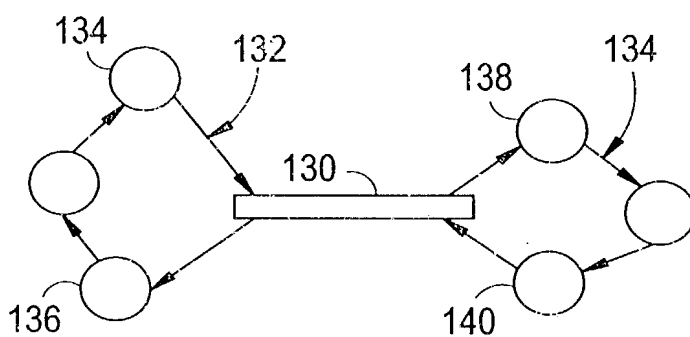


FIG. 13



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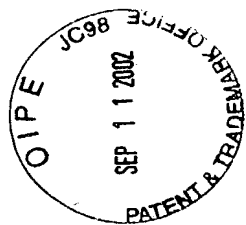


FIG. 14

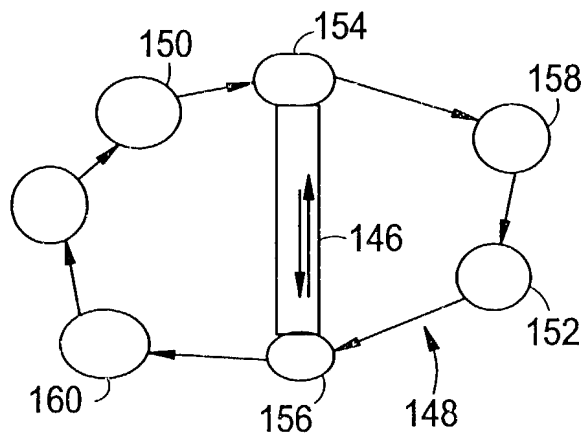


FIG. 15

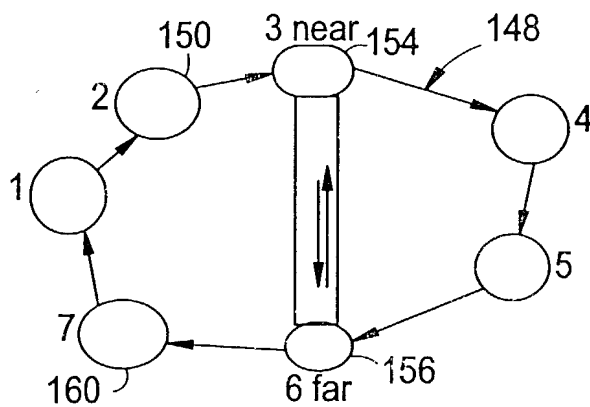
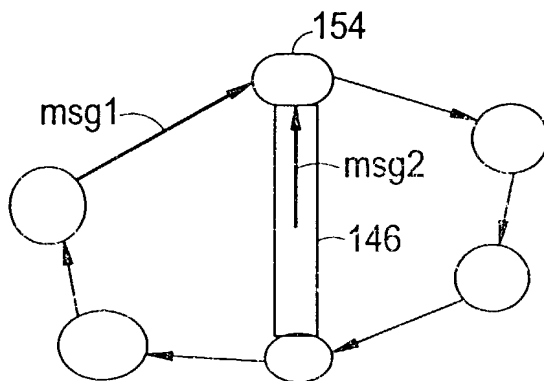
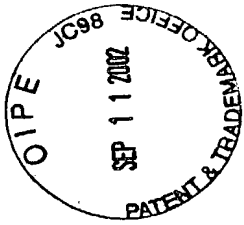


FIG. 16





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FIG. 17

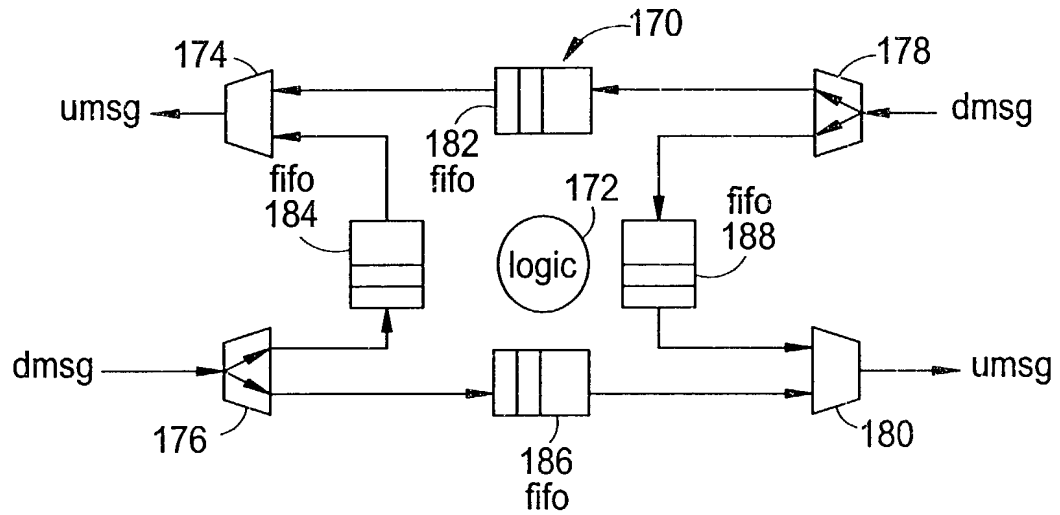


FIG. 18

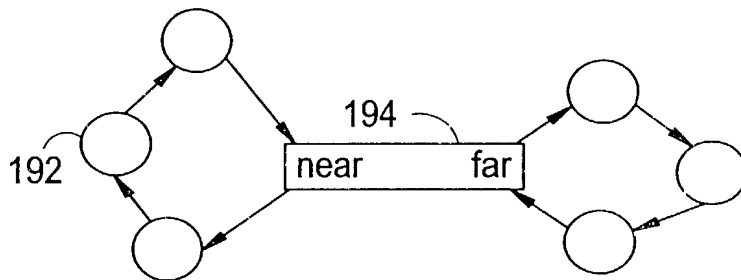
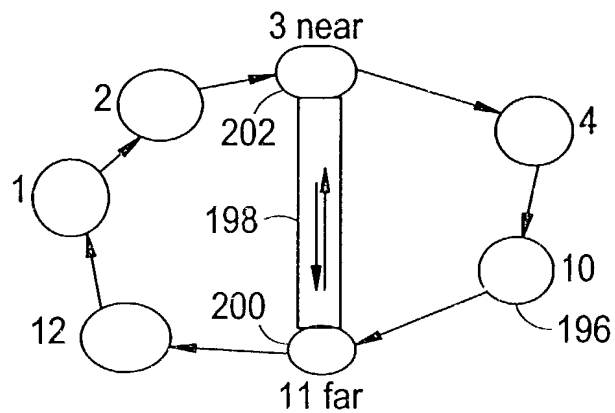
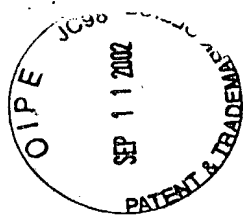


FIG. 19





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FIG. 20

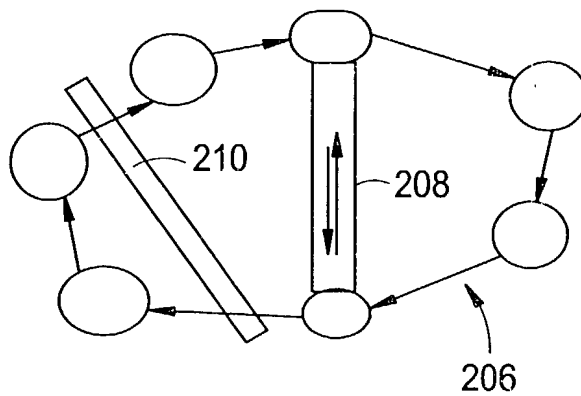


FIG. 21

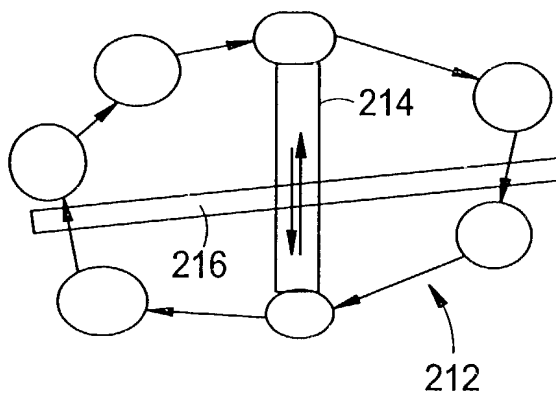


FIG. 22

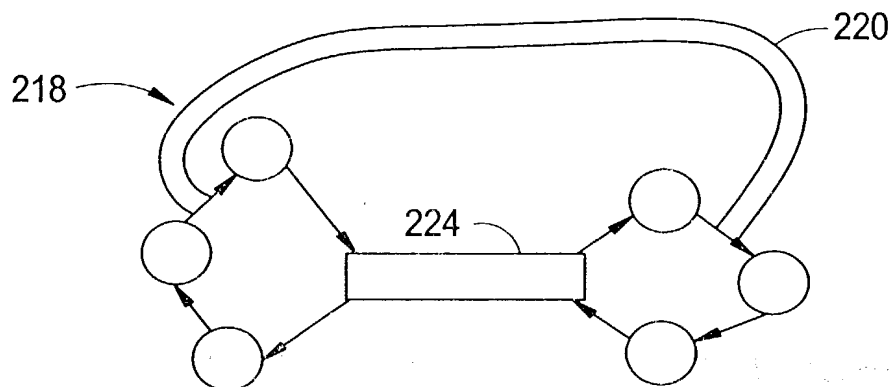
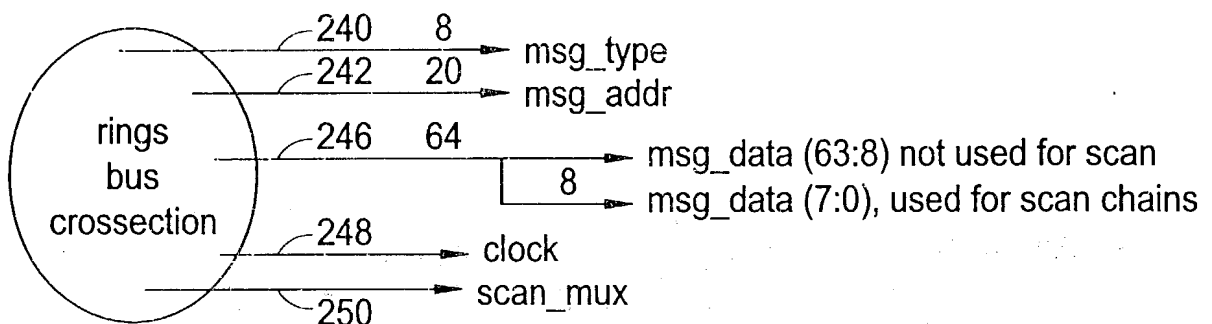


FIG. 23





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FIG. 26

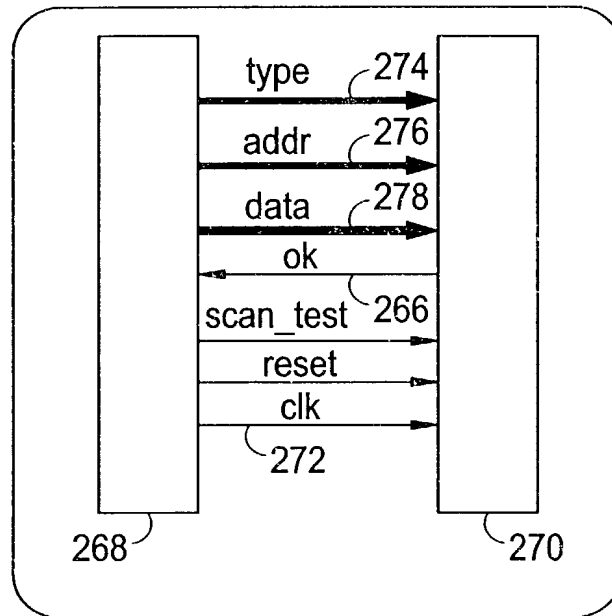


FIG. 27

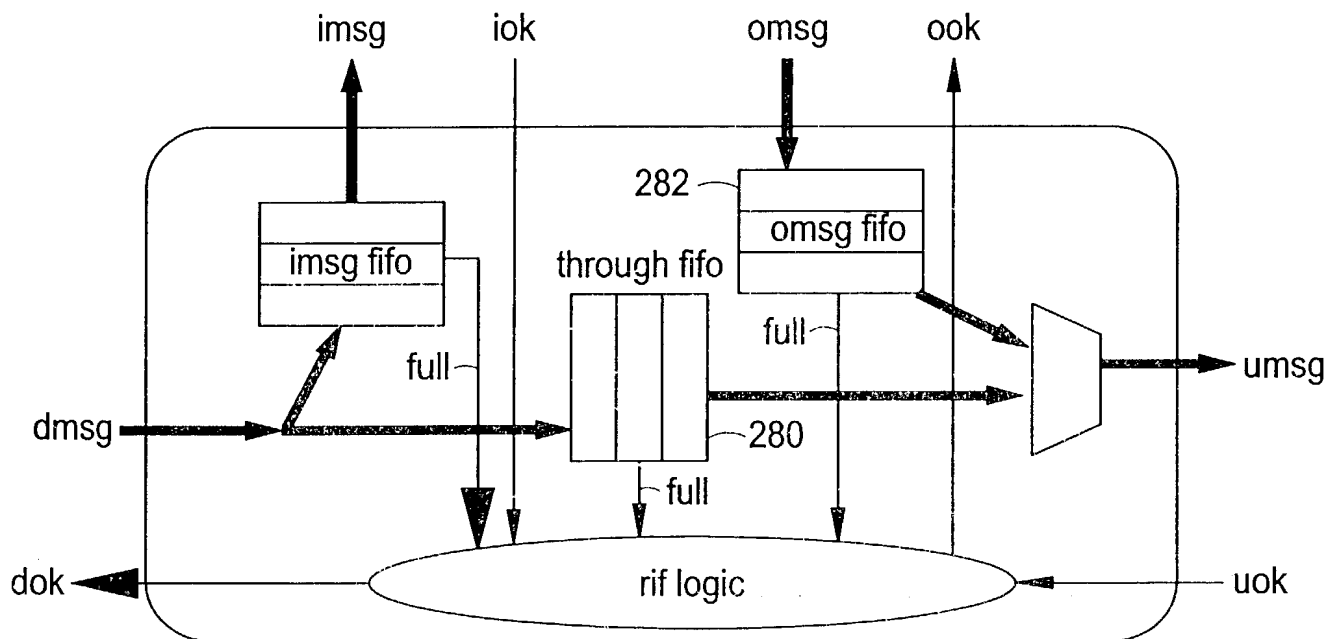
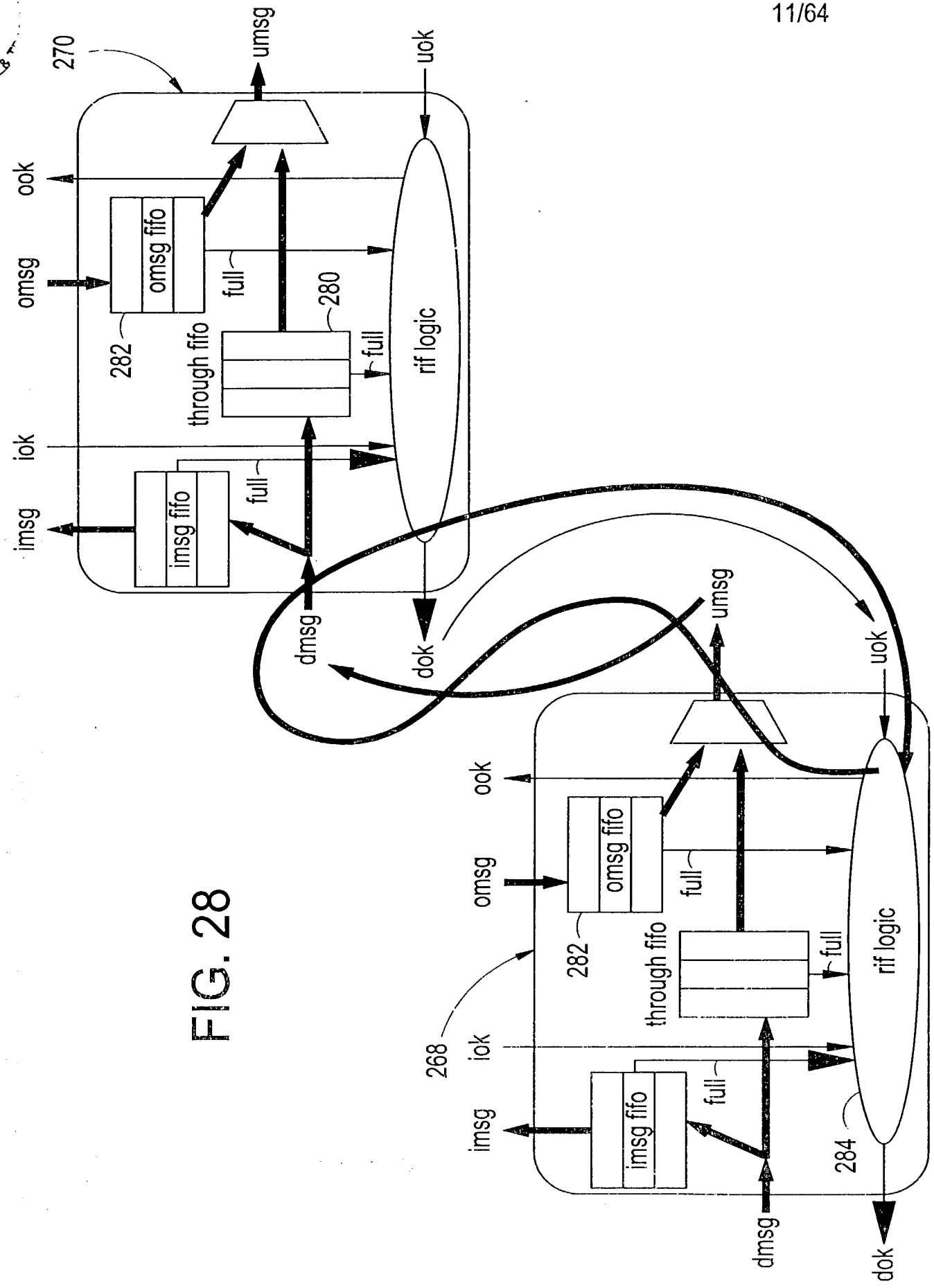
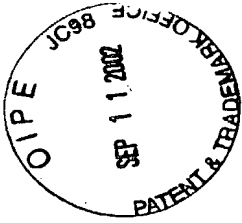


FIG. 28

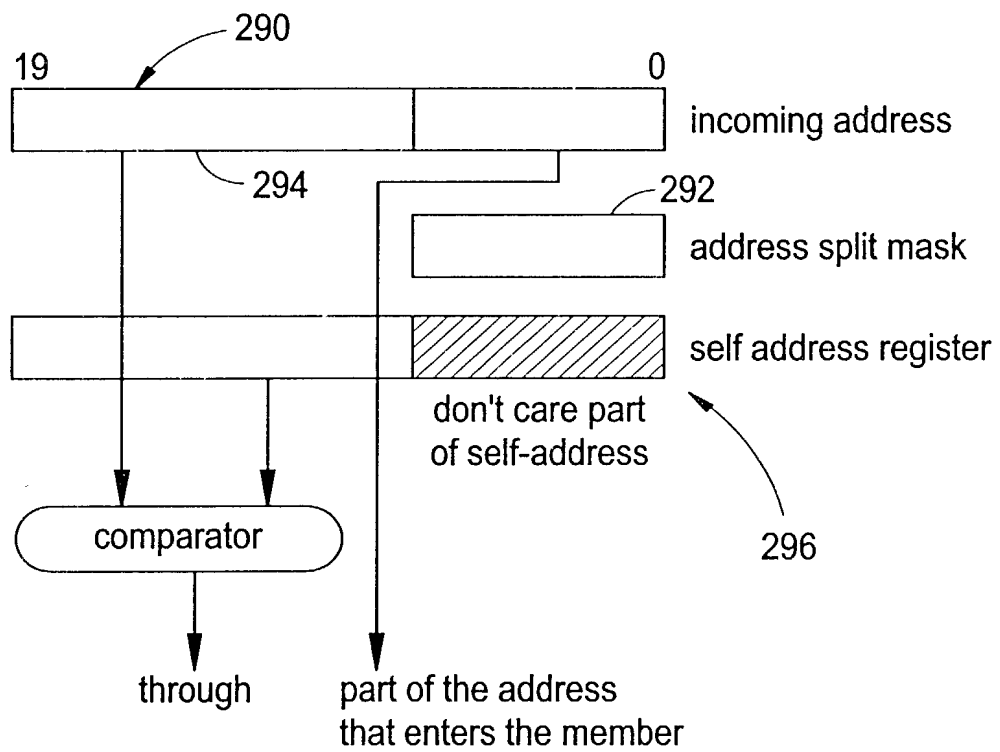


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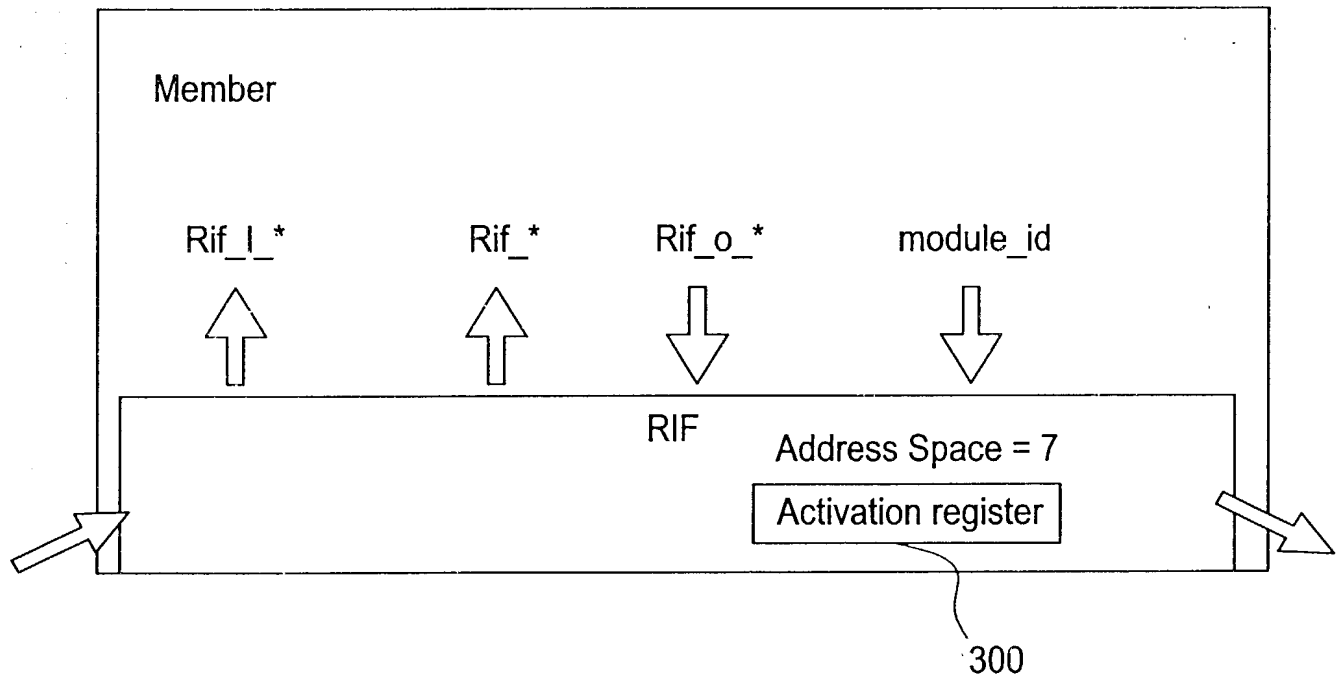
FIG. 29

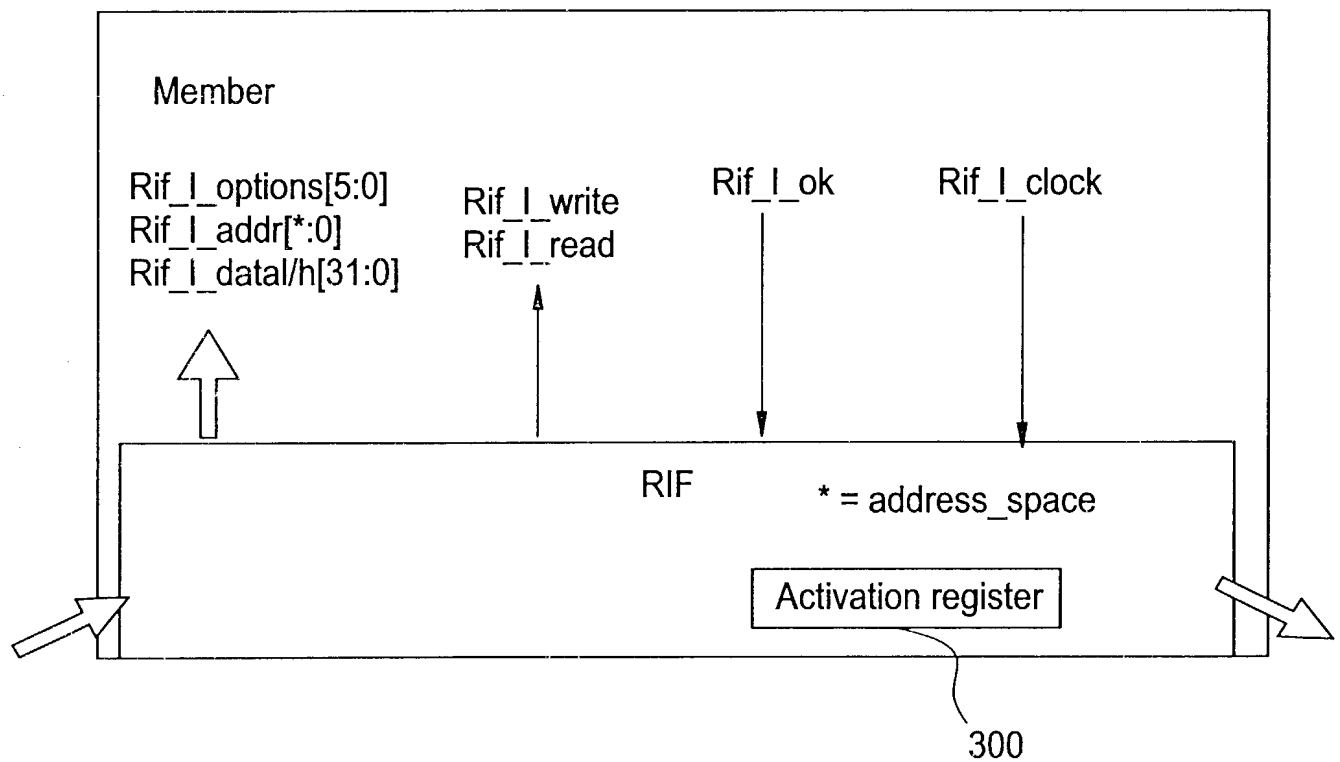


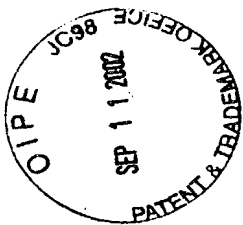


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FIG. 30

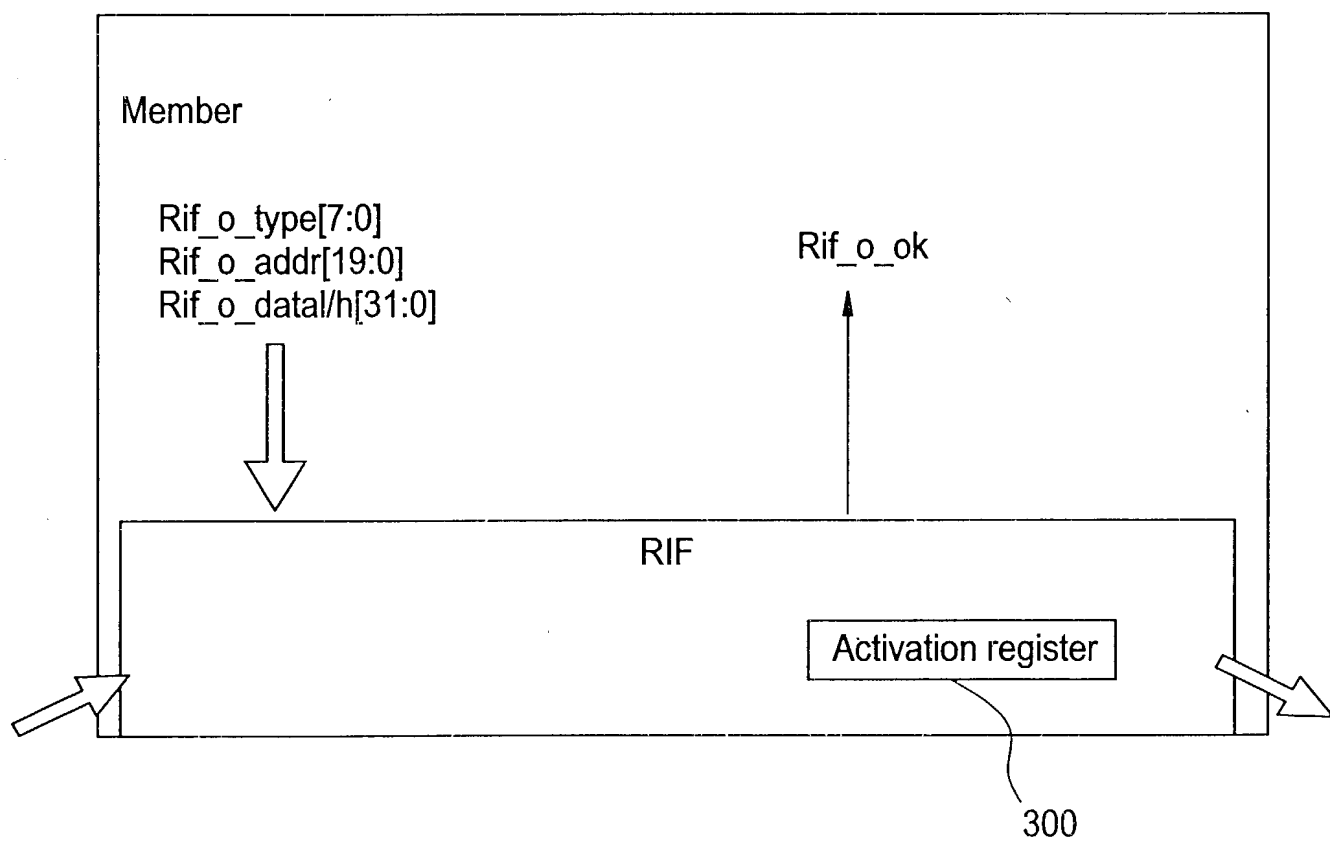






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FIG. 32



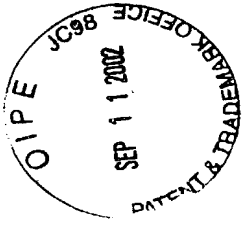


FIG. 33

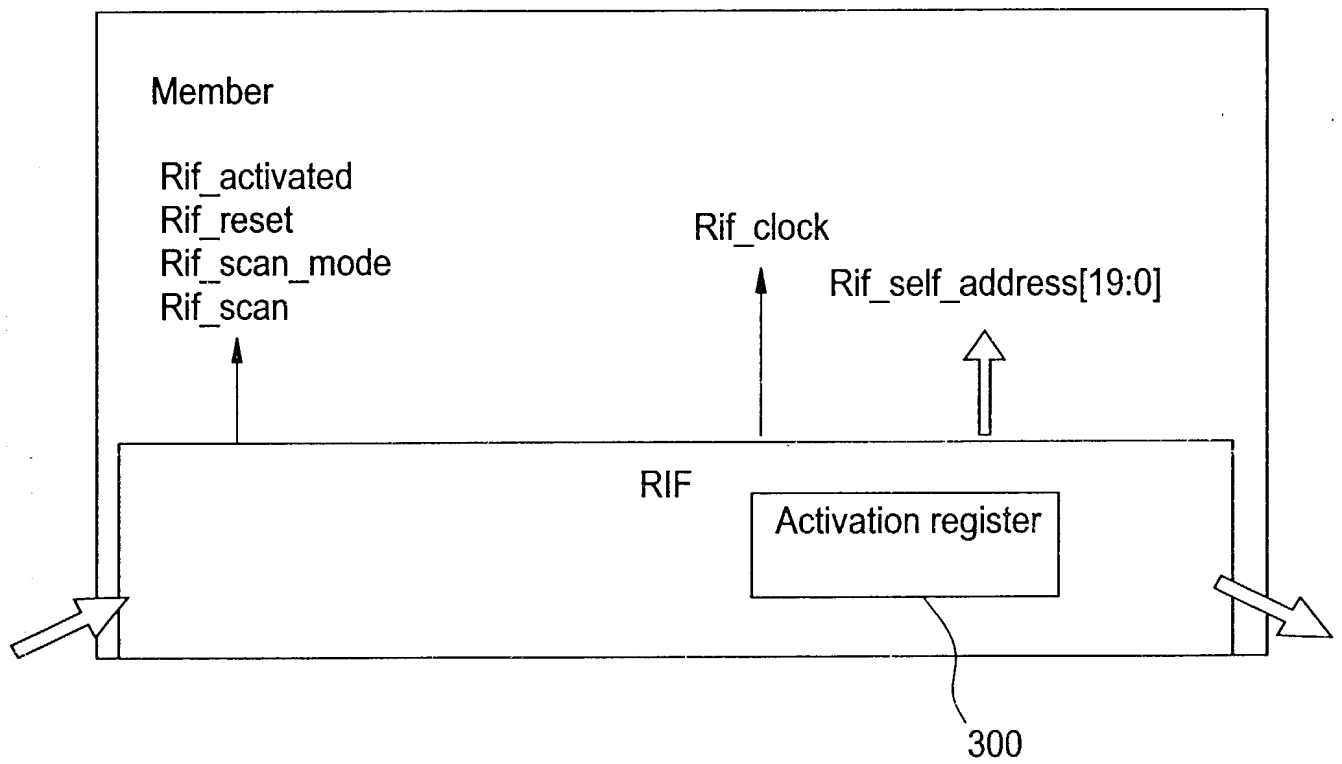


FIG. 34

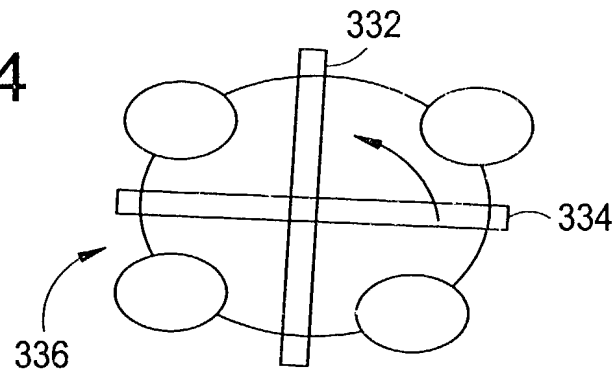


FIG. 35

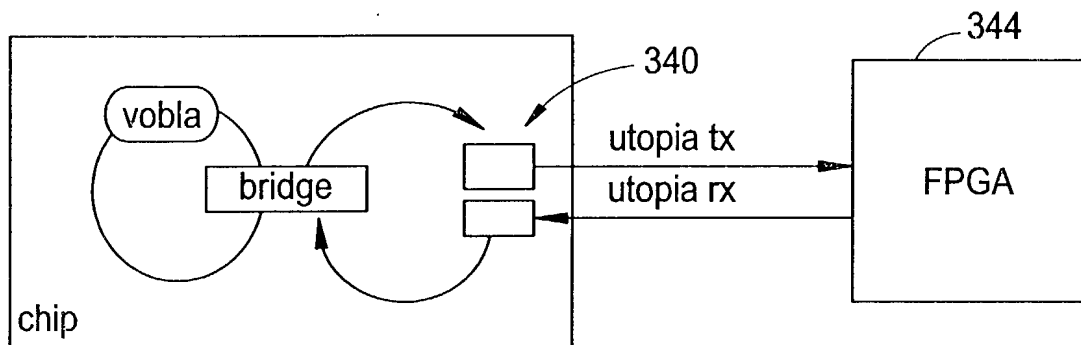


FIG. 36

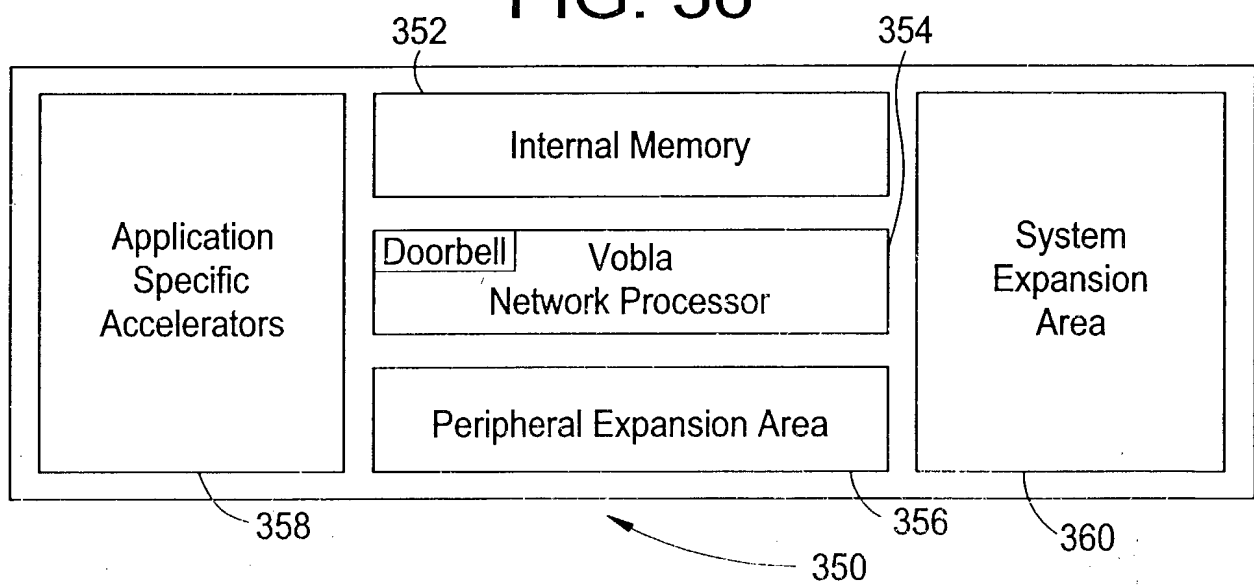


FIG. 37

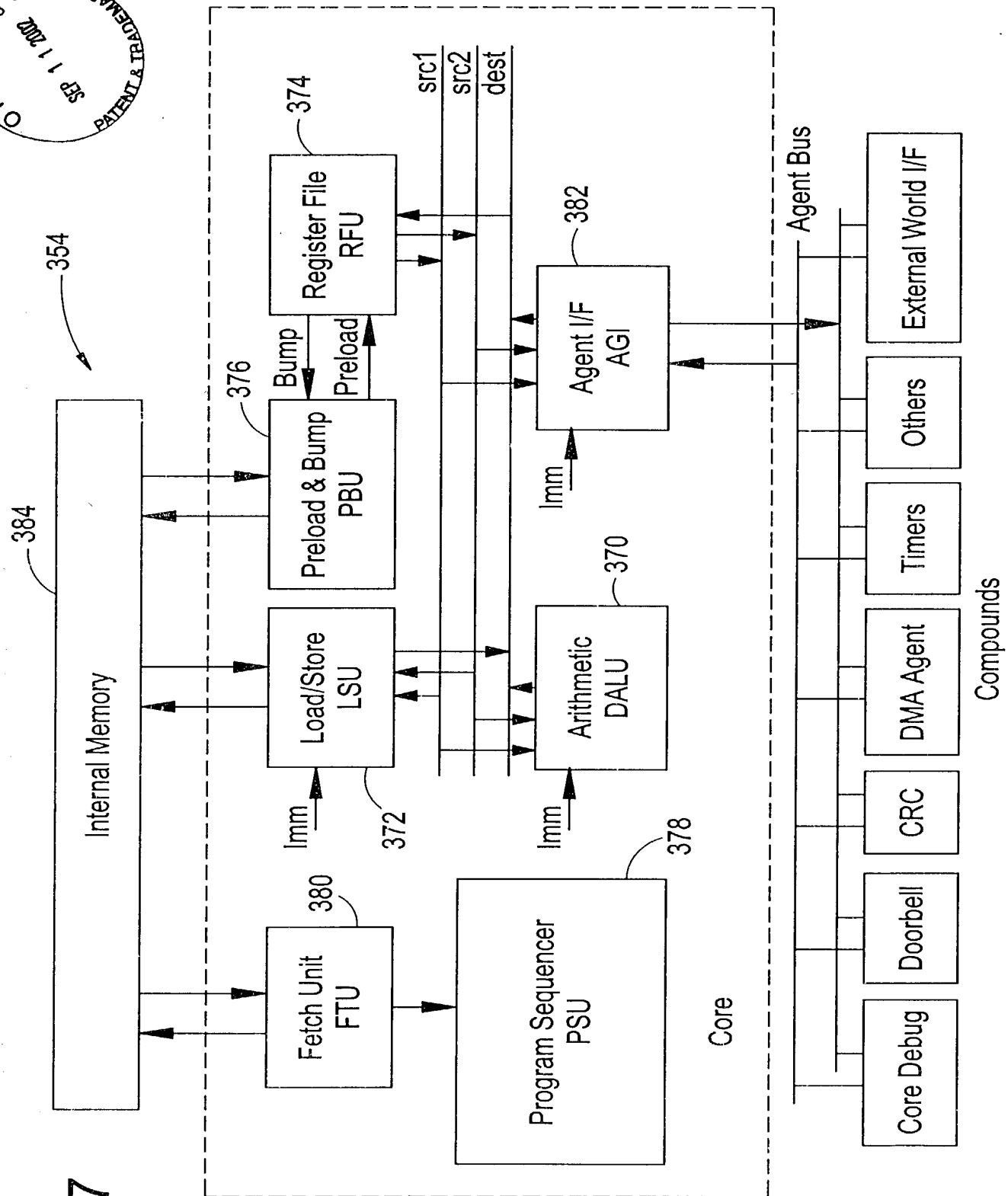
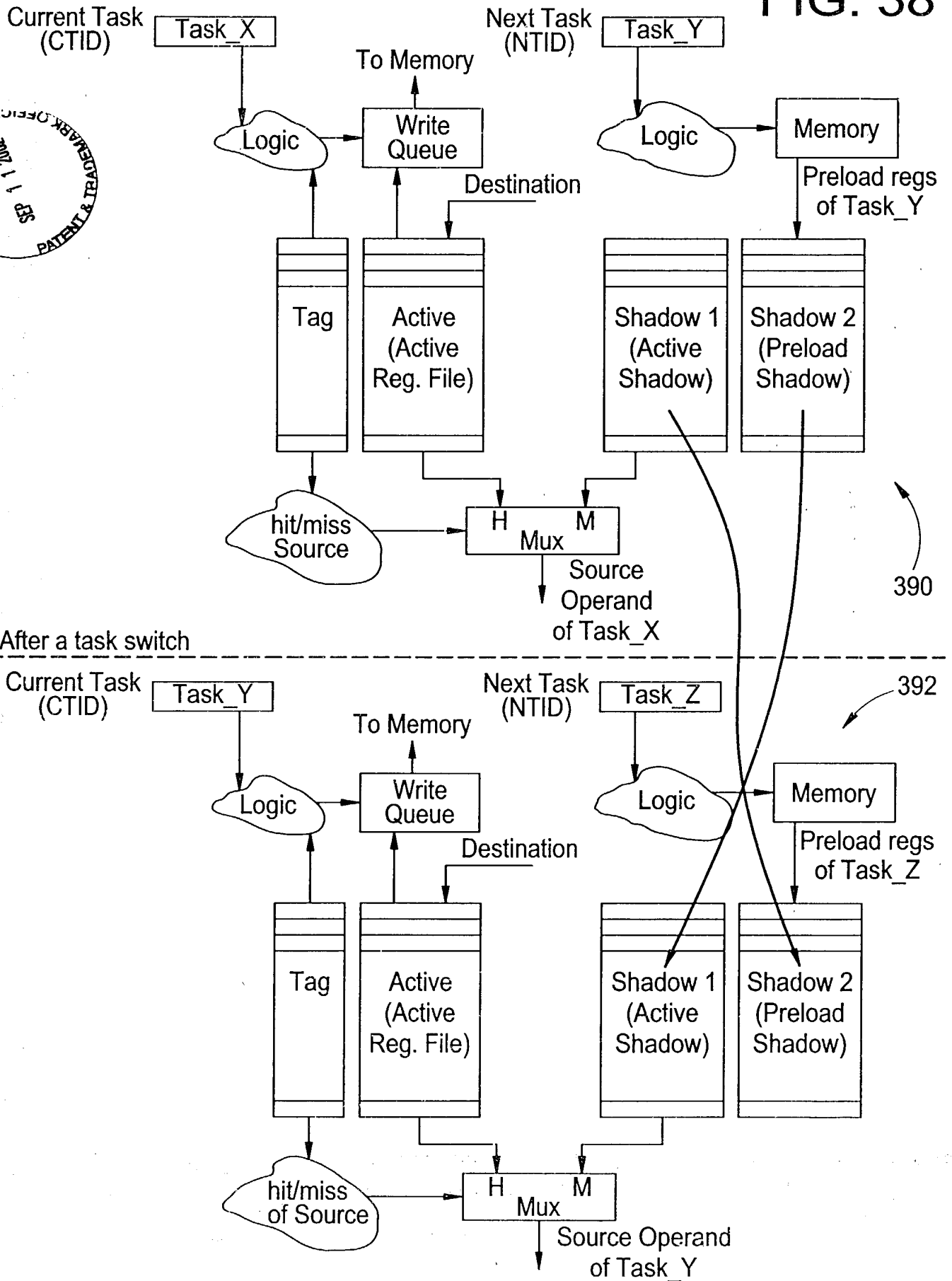
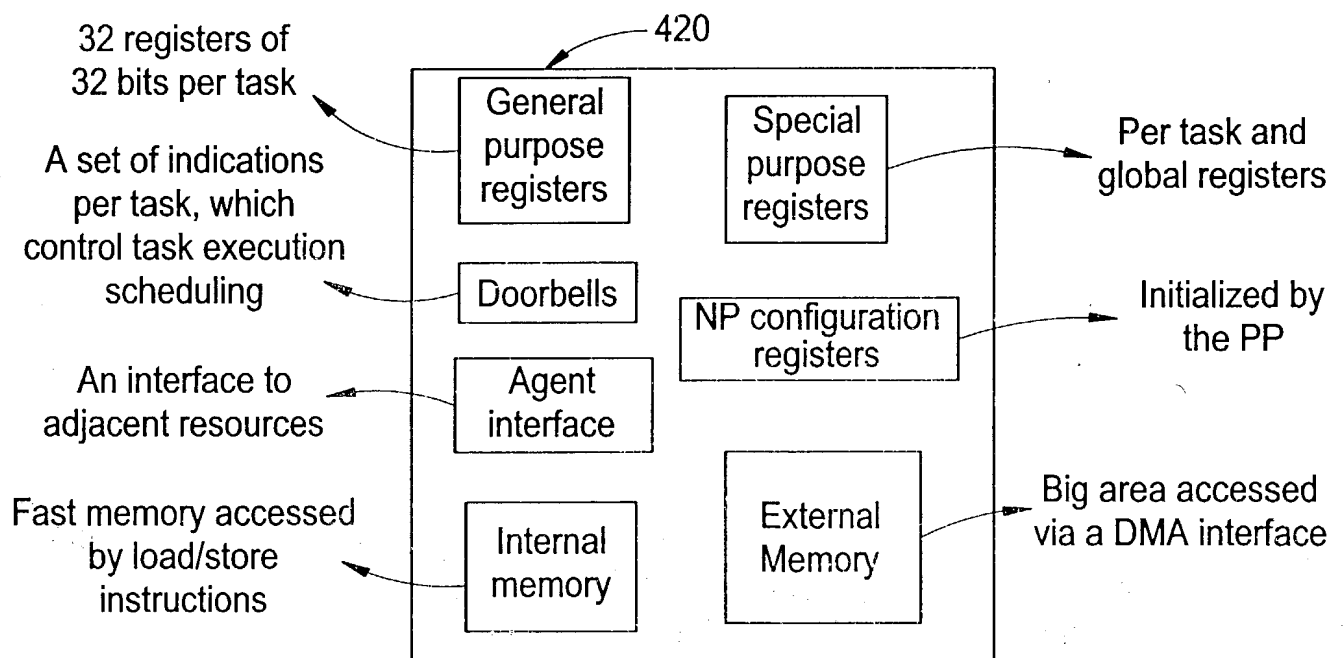


FIG. 38





3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0



mb - reflection of the RAM mult-reader busy indication

430

FIG. 42

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

440

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

DOOR BELL REQ			COUNT			U R	MASK		N T V		NTID		CTID
---------------------	--	--	-------	--	--	--------	------	--	-------------	--	------	--	------

442

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

[illegible]

444

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

446

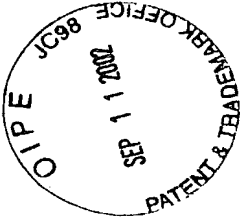
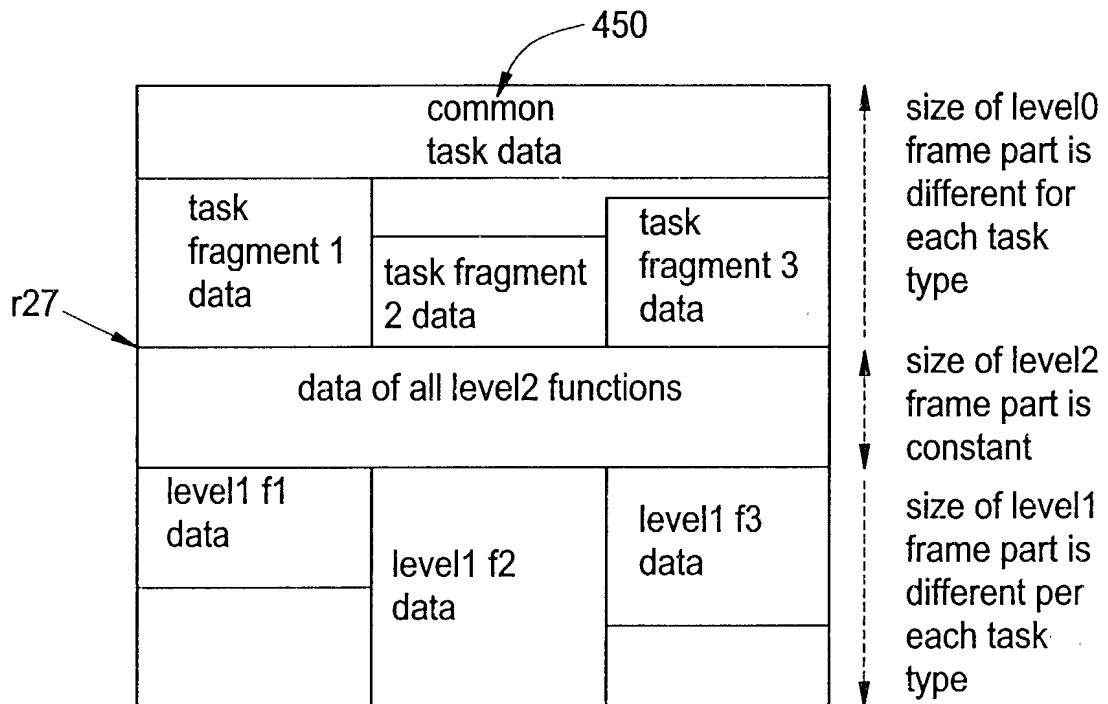


FIG. 43



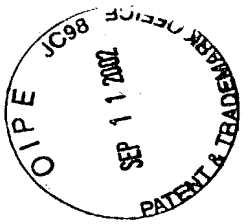
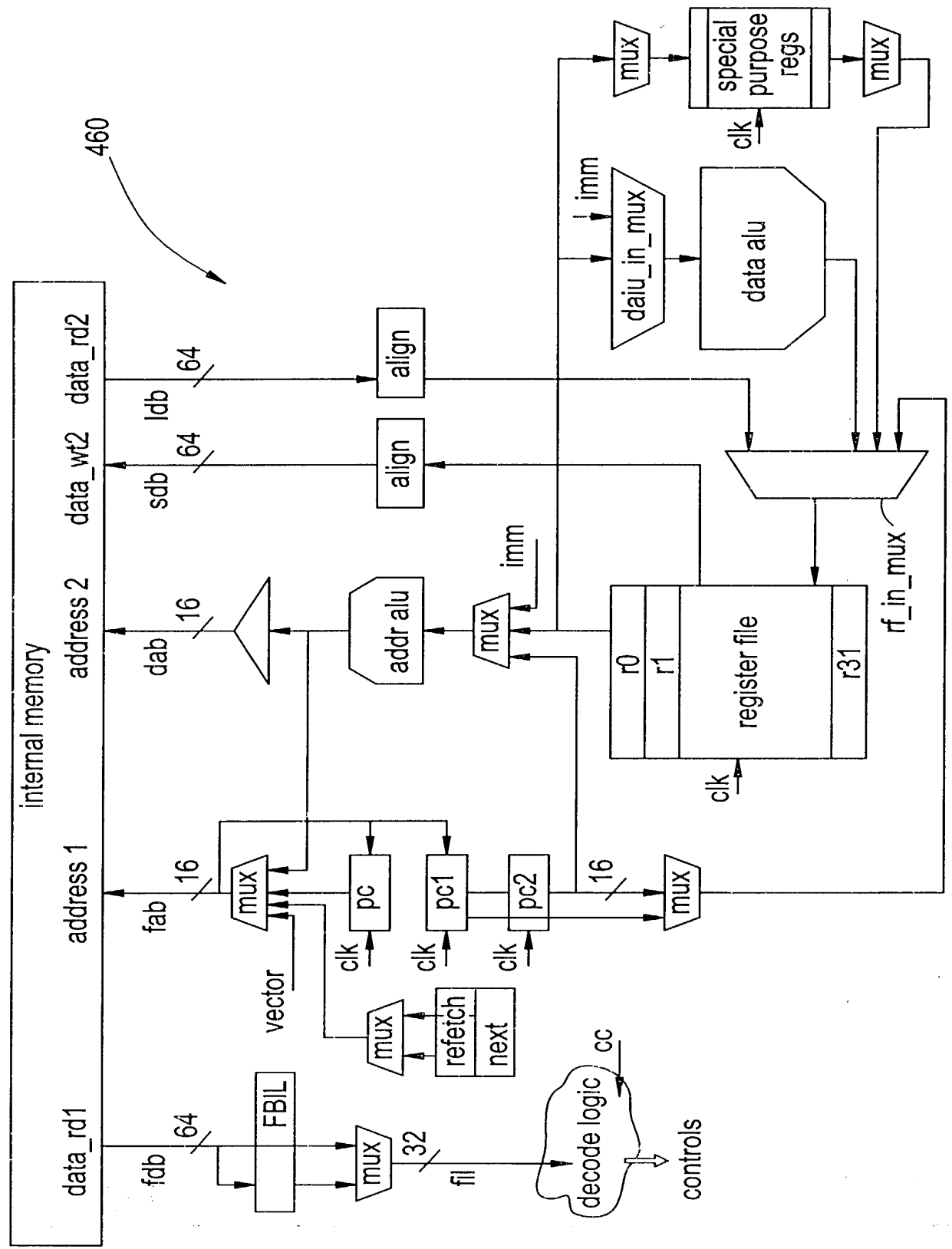


FIG. 44



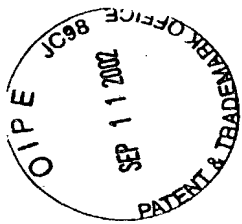
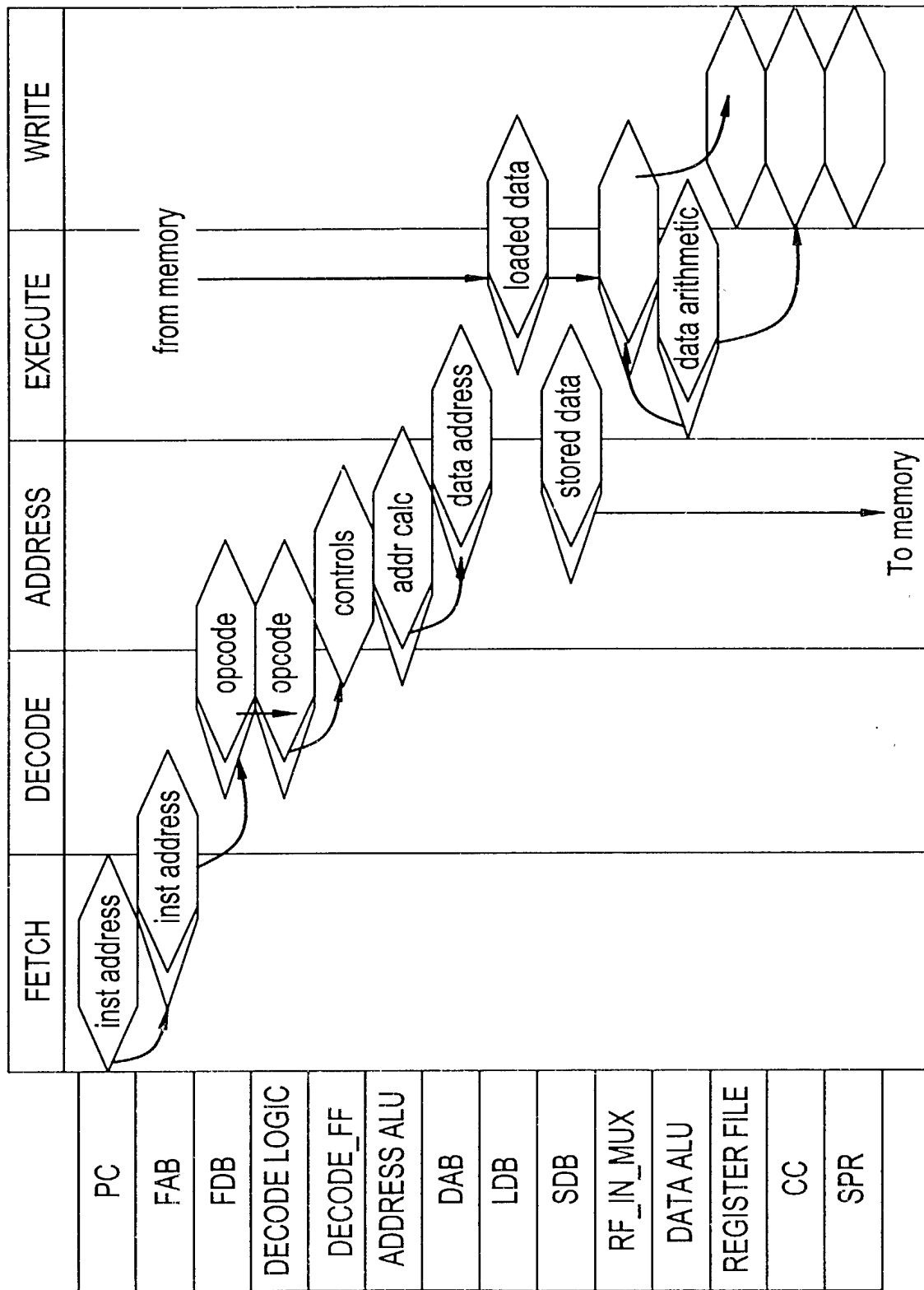


FIG. 45

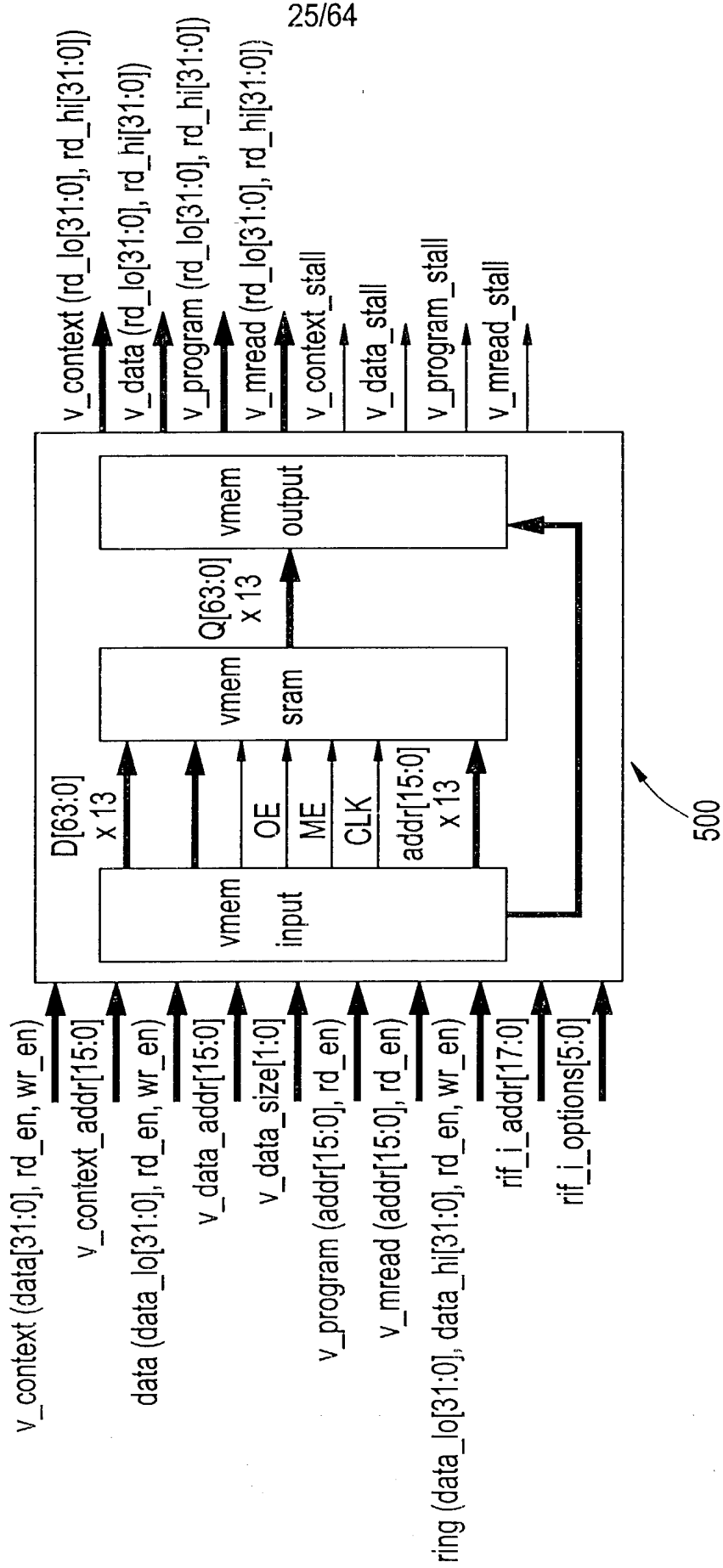


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FIG. 46



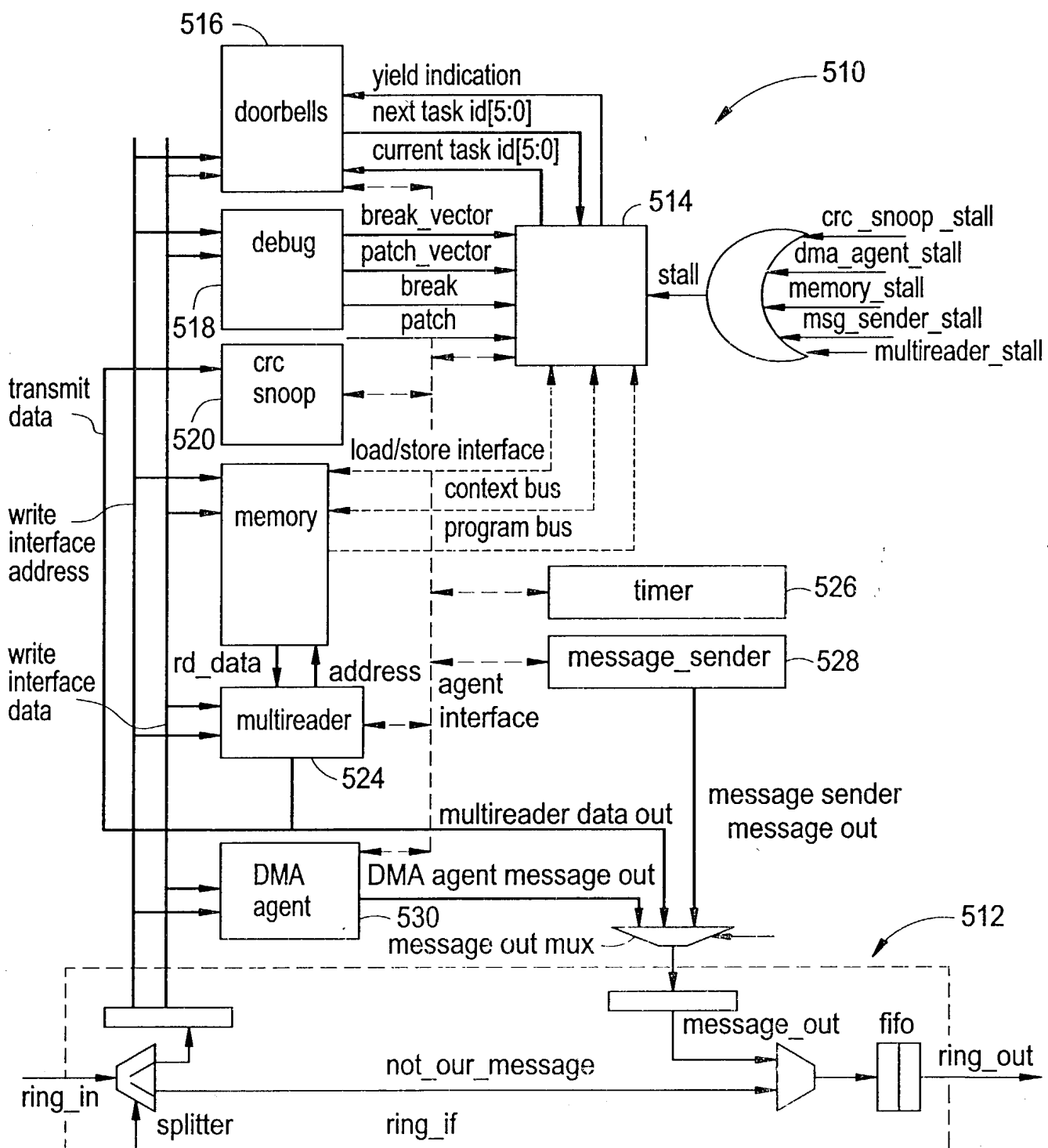


FIG. 48

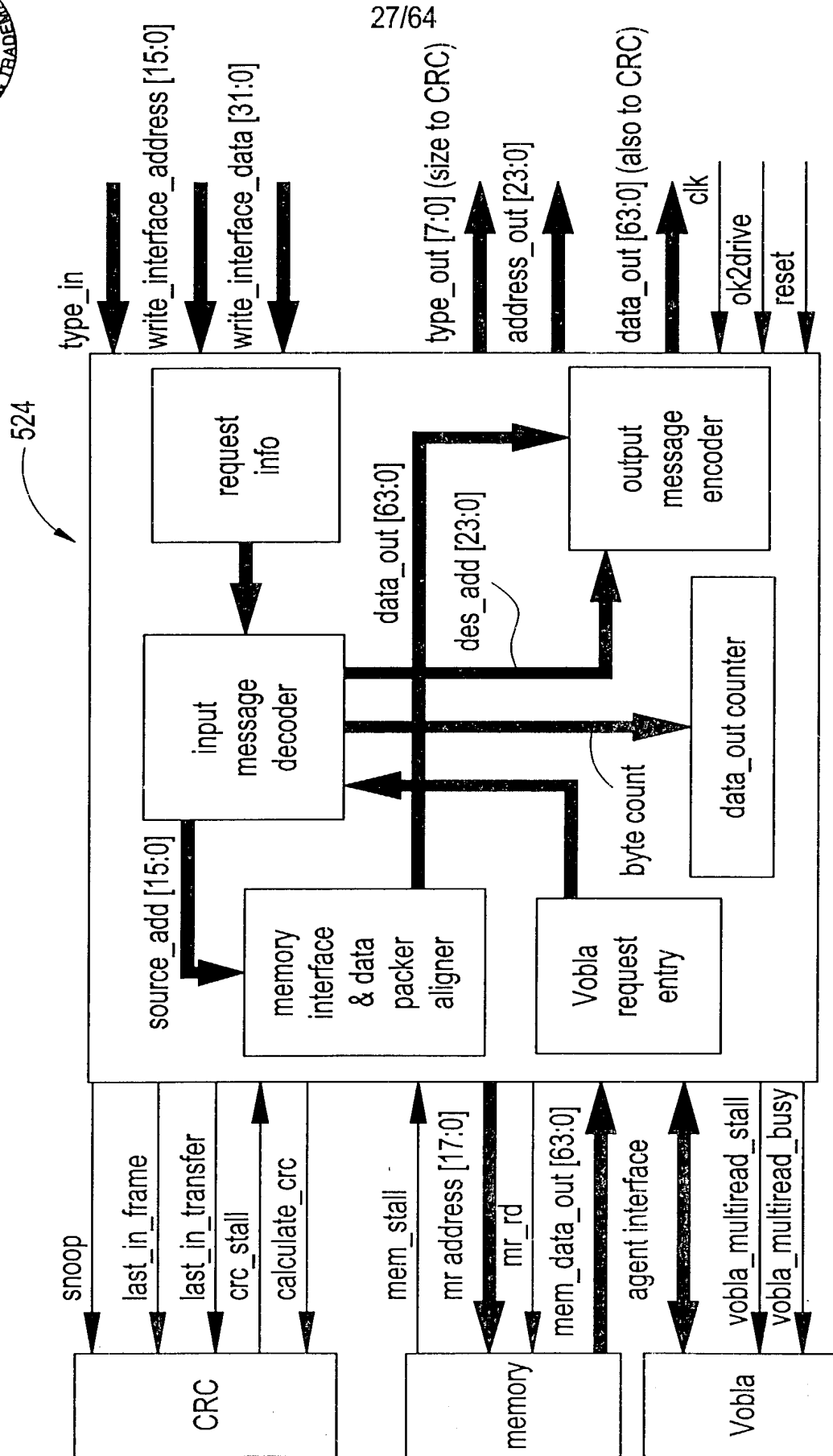
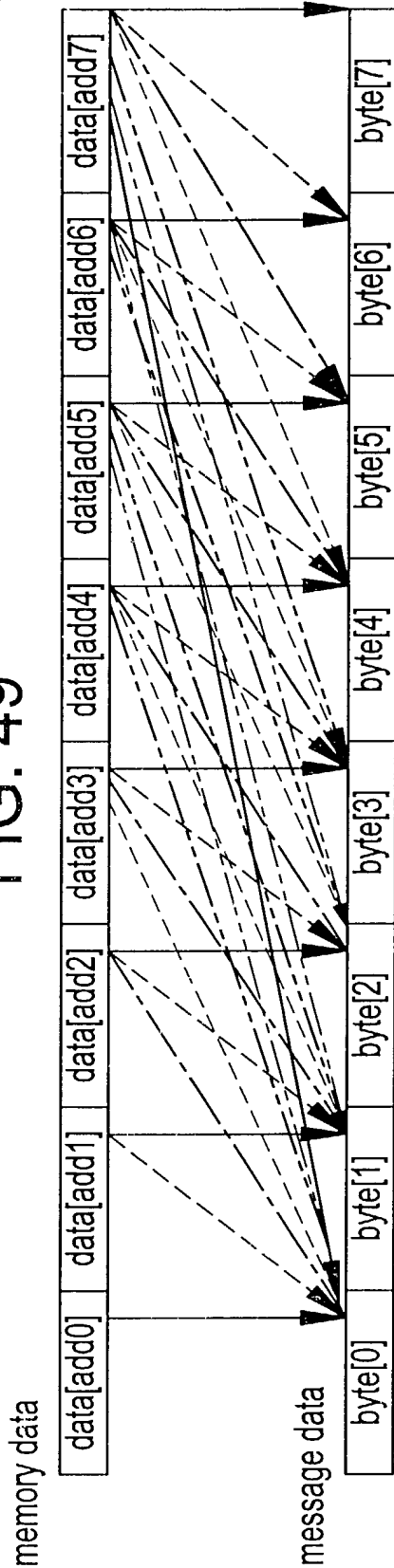
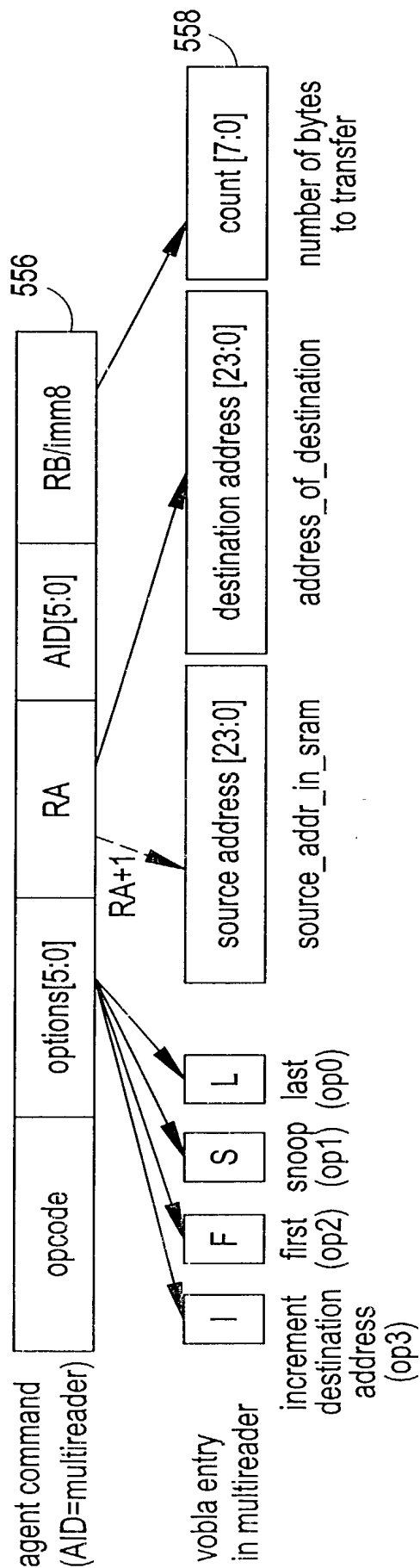


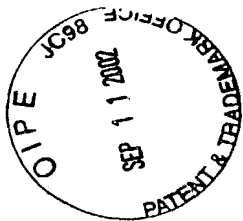
FIG. 49



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FIG. 50





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FIG. 51

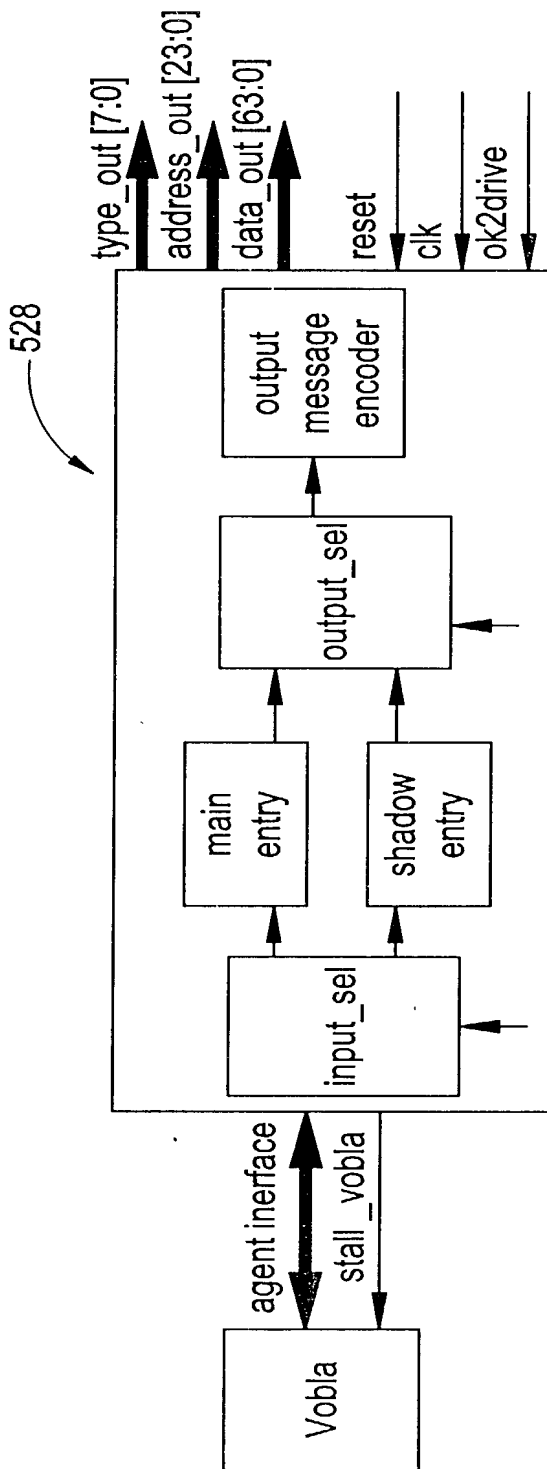
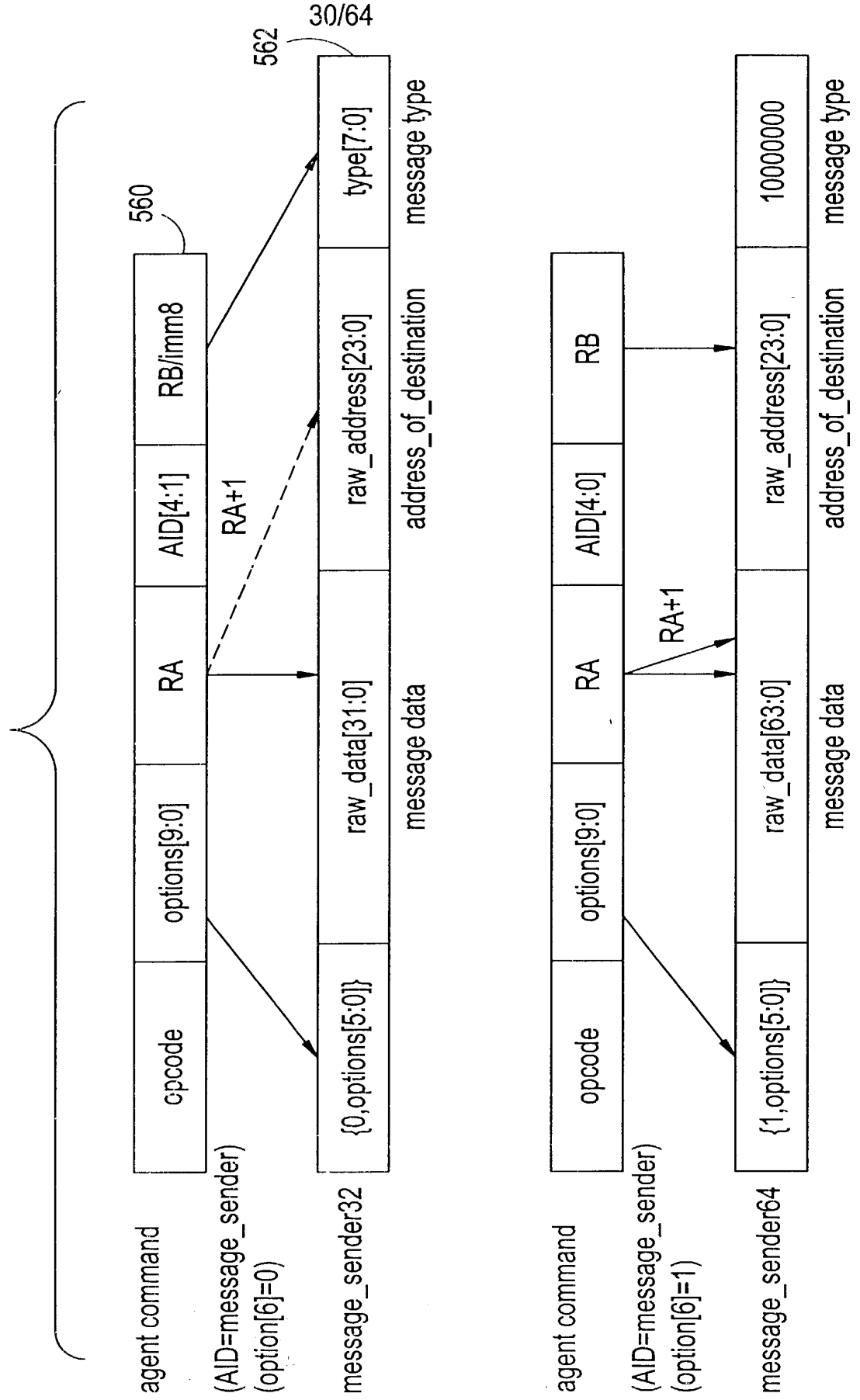




FIG. 52



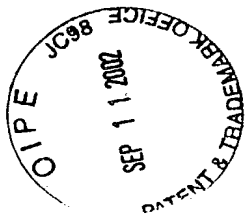
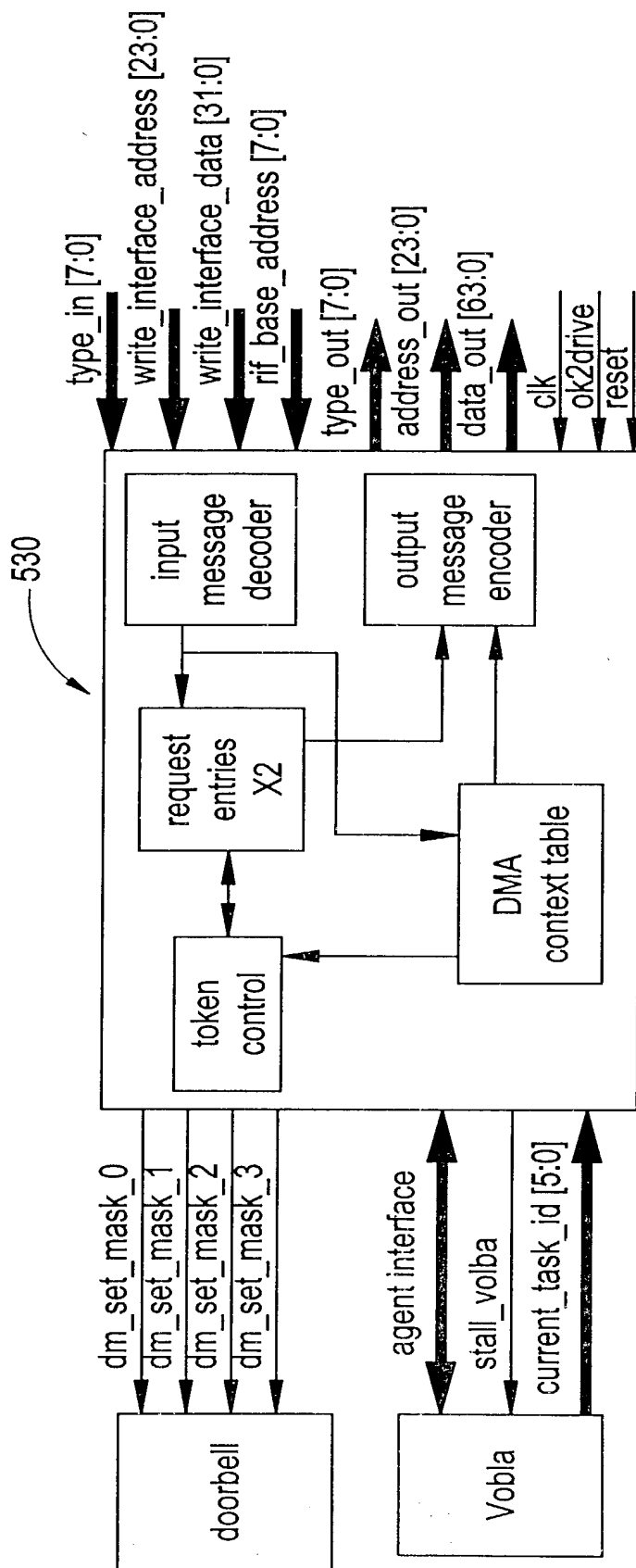


FIG. 53



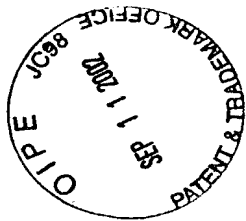
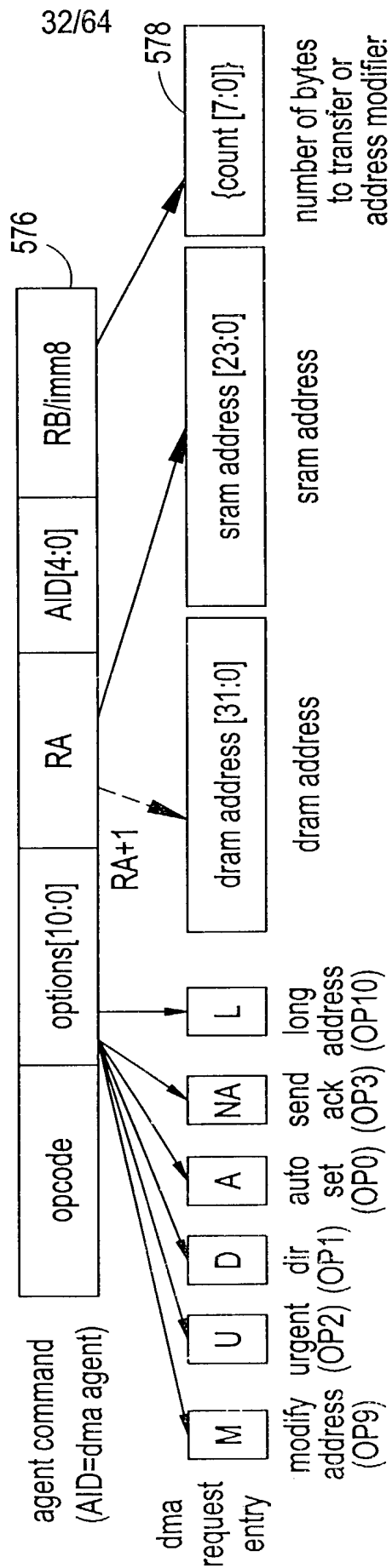


FIG. 54



11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

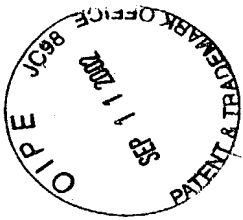
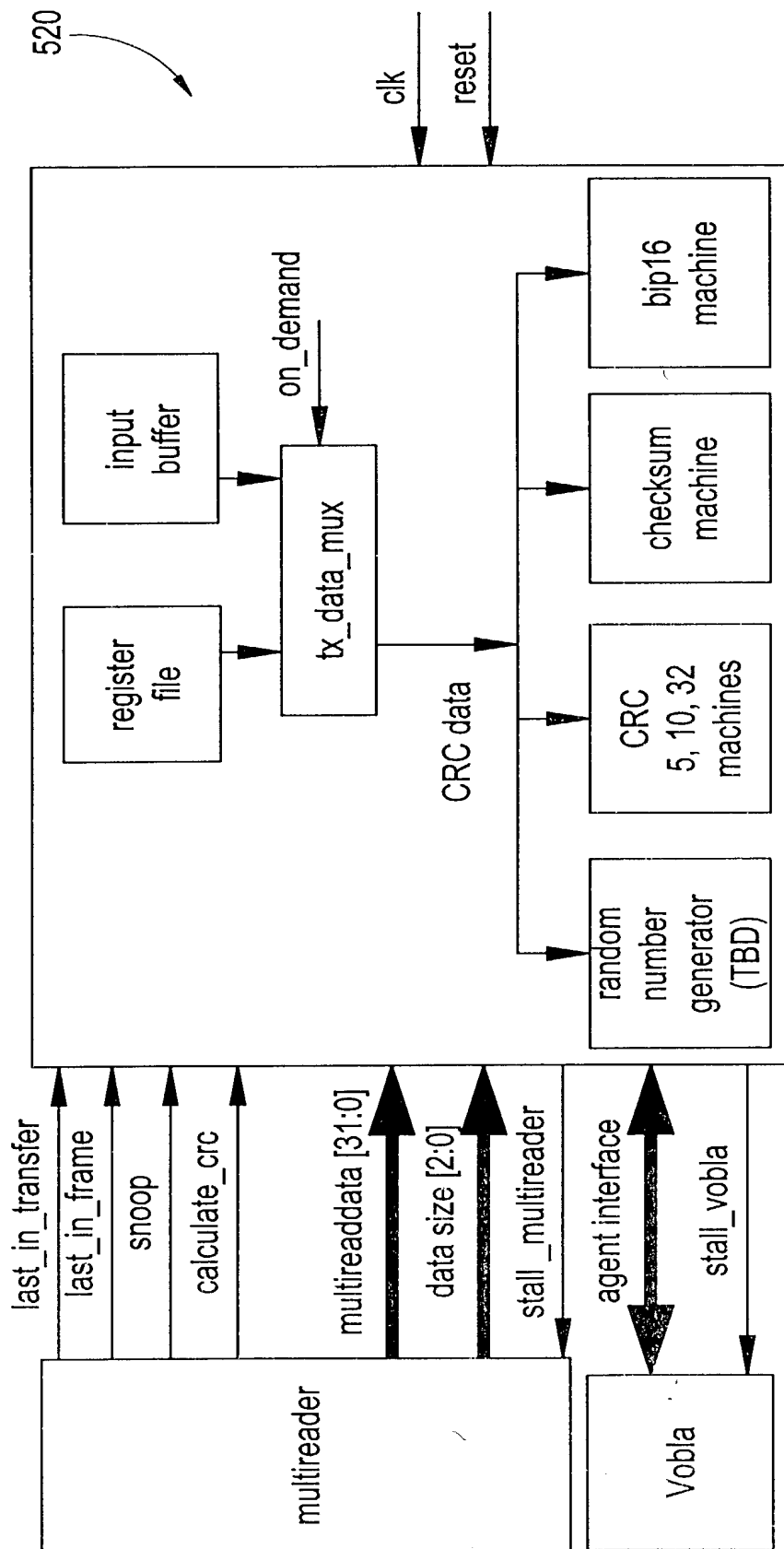


FIG. 55



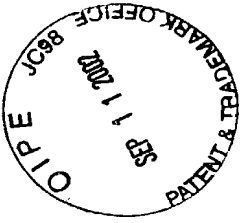
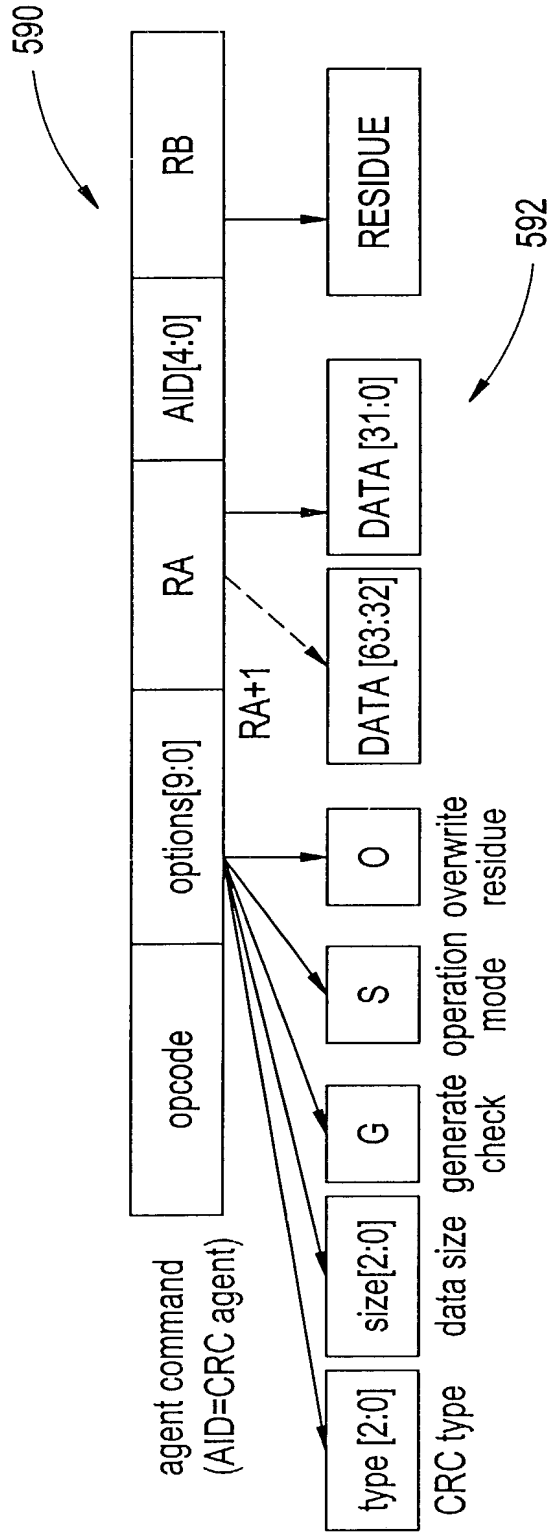


FIG. 56





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FIG. 57

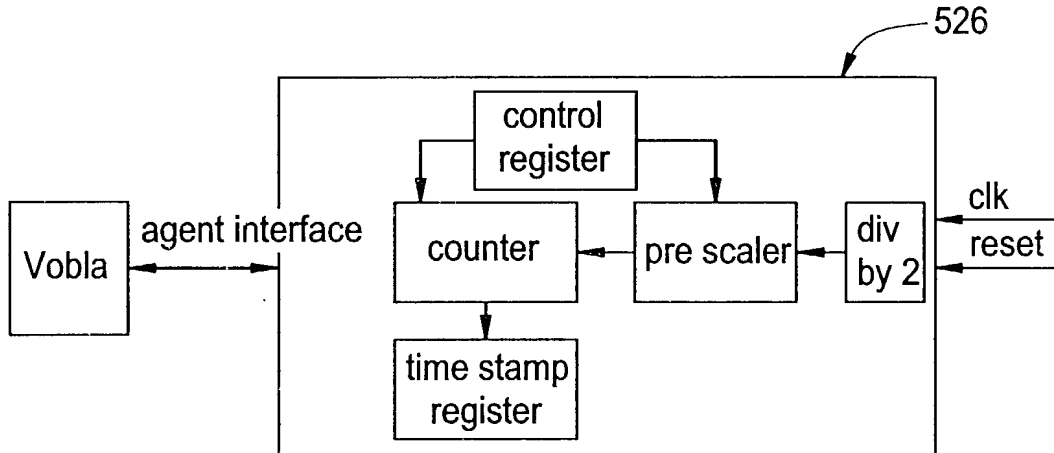
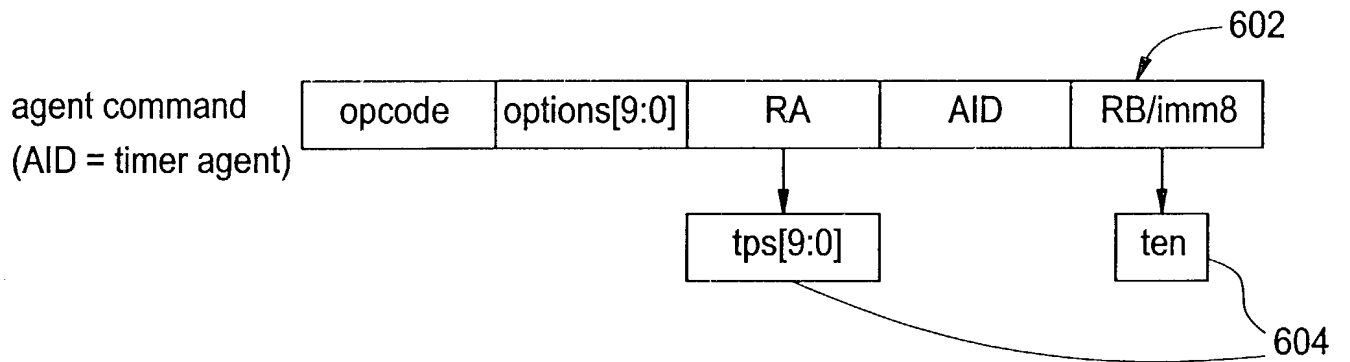


FIG. 58



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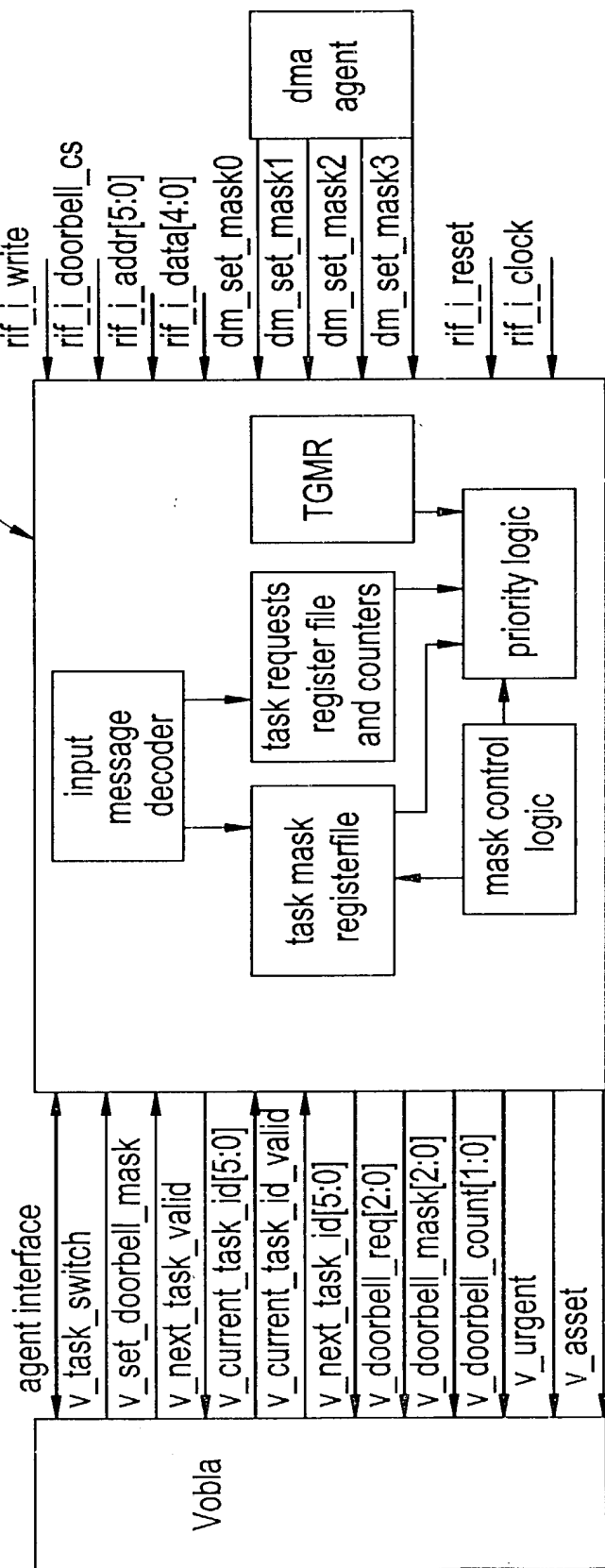


FIG. 60

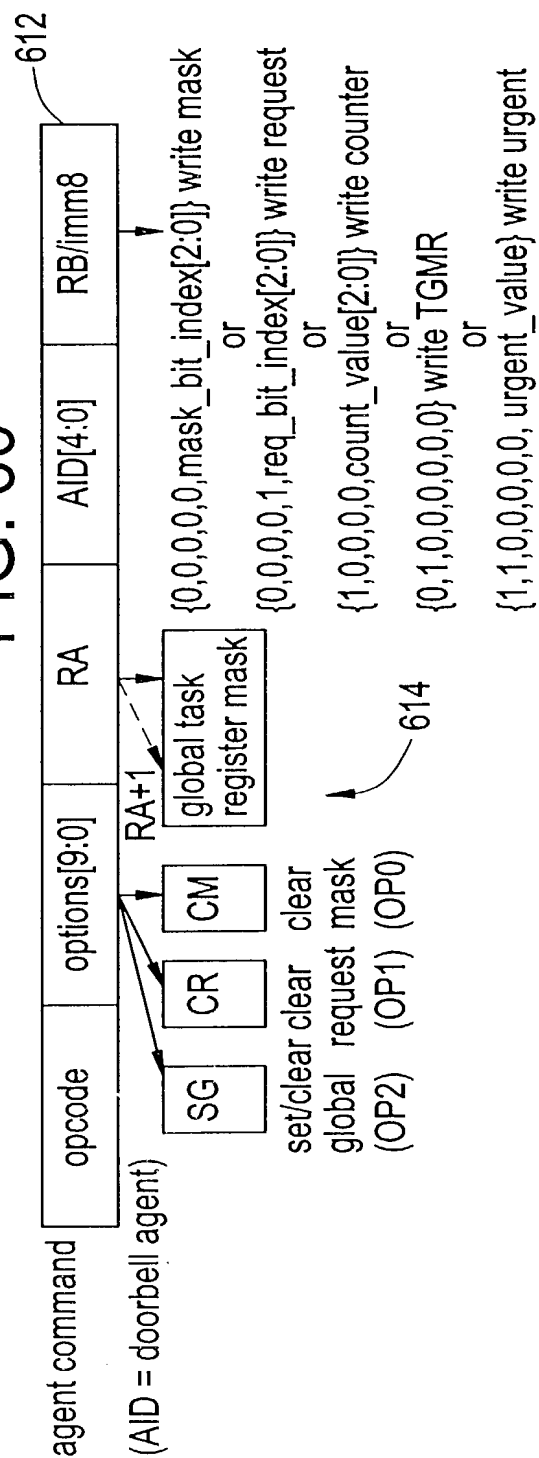


FIG. 61

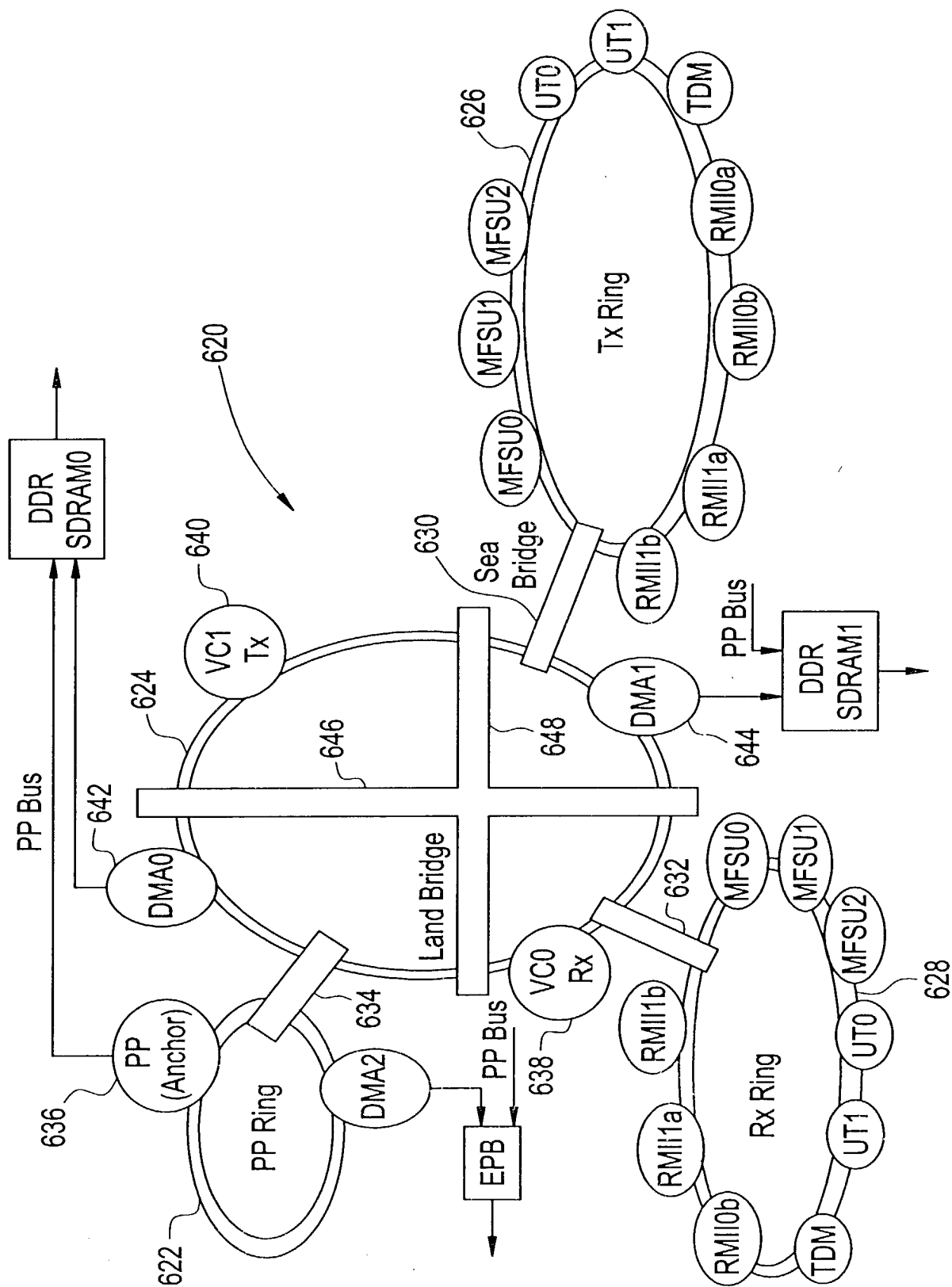
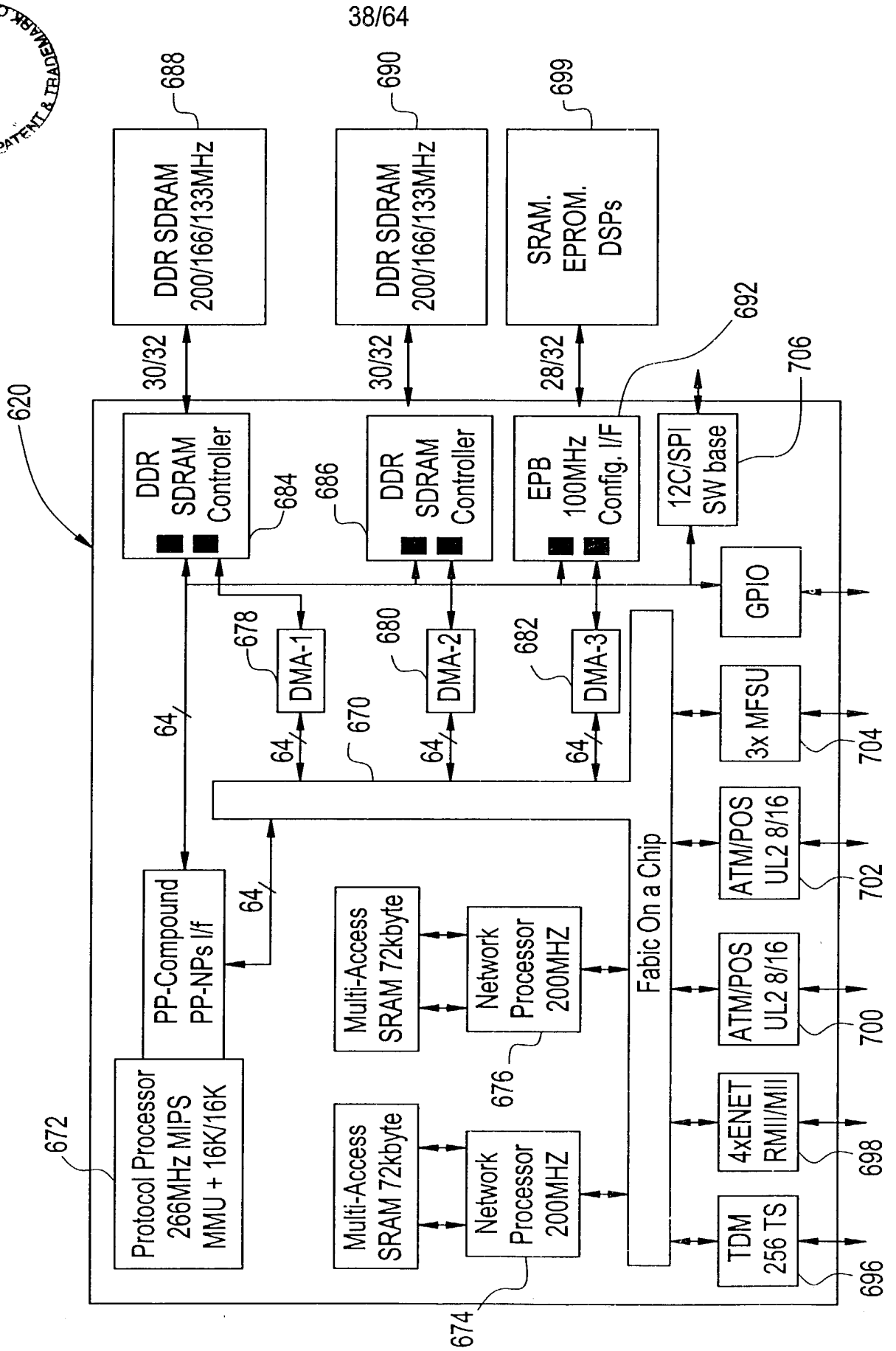
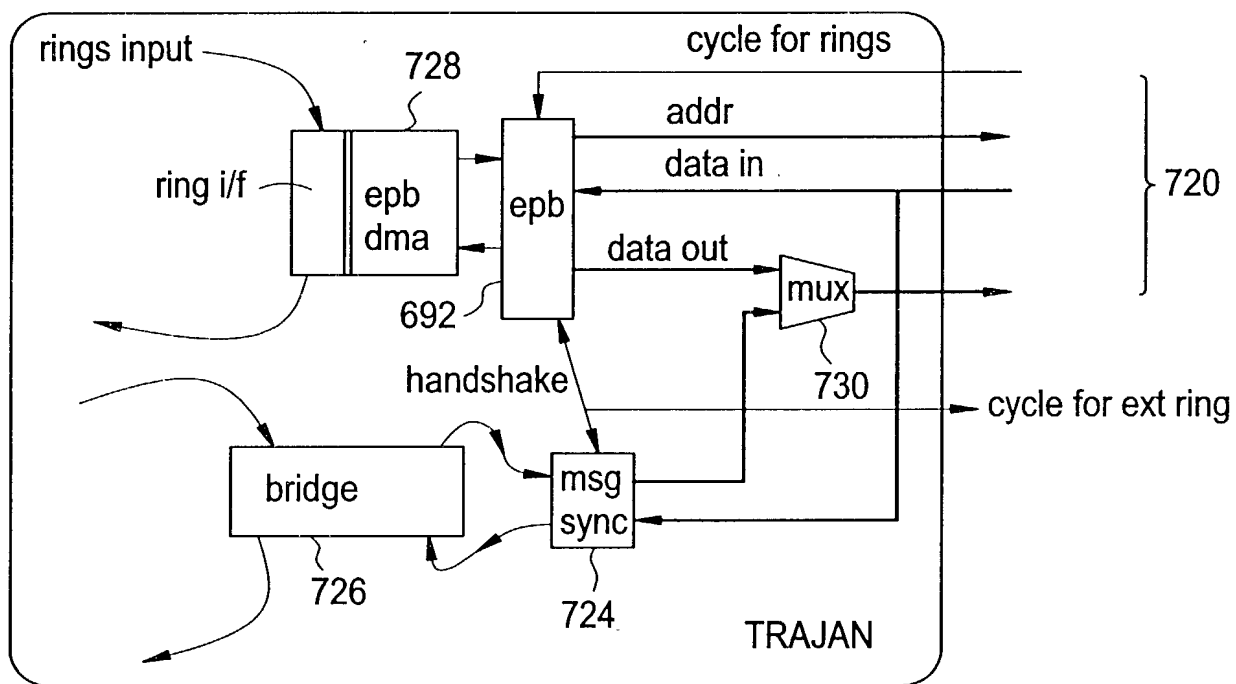


FIG. 62





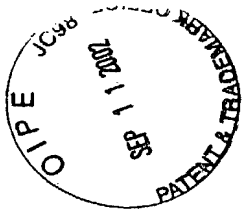
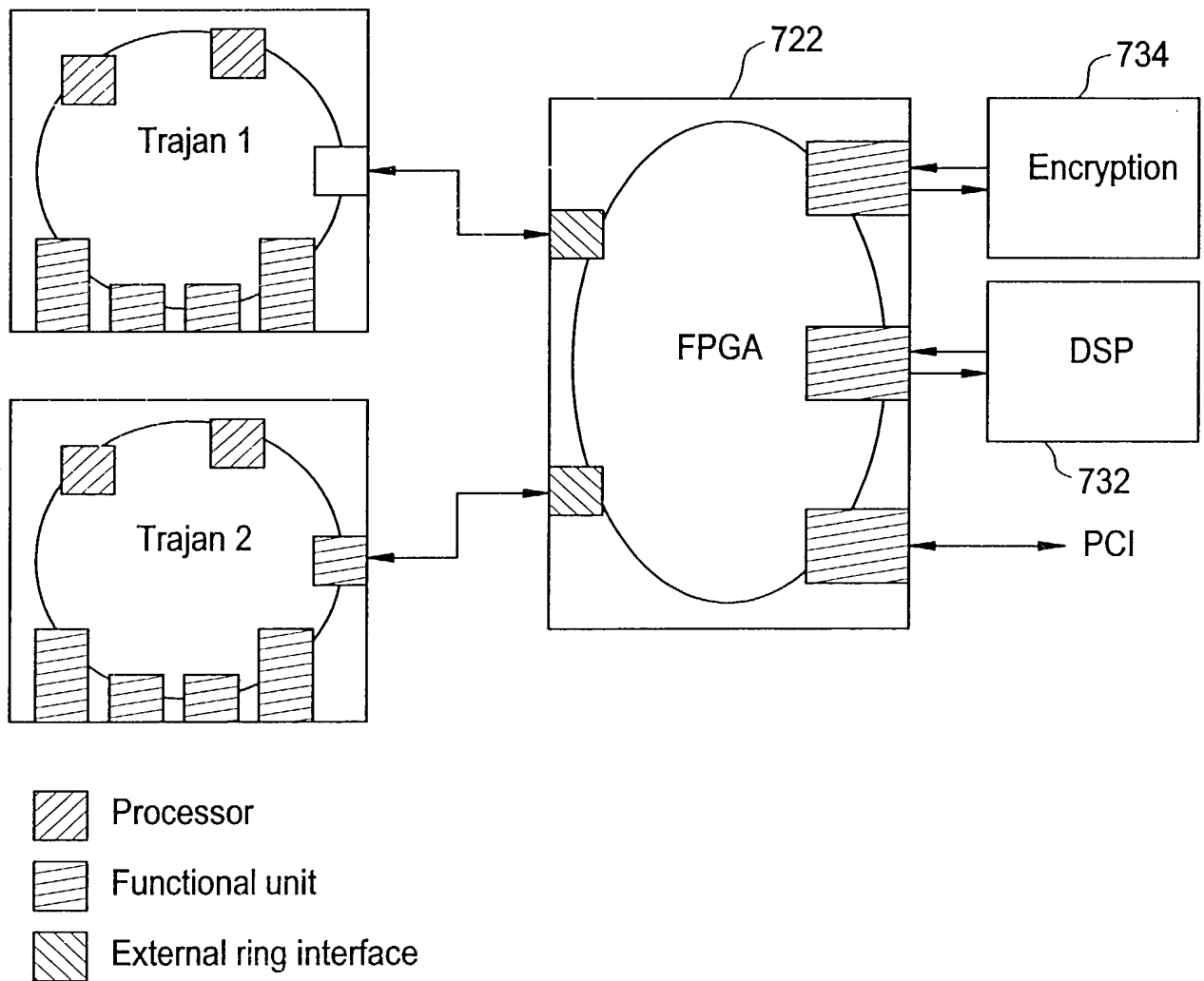


FIG. 64



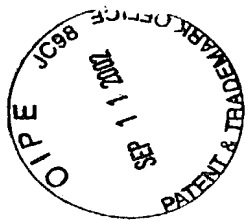


FIG. 65

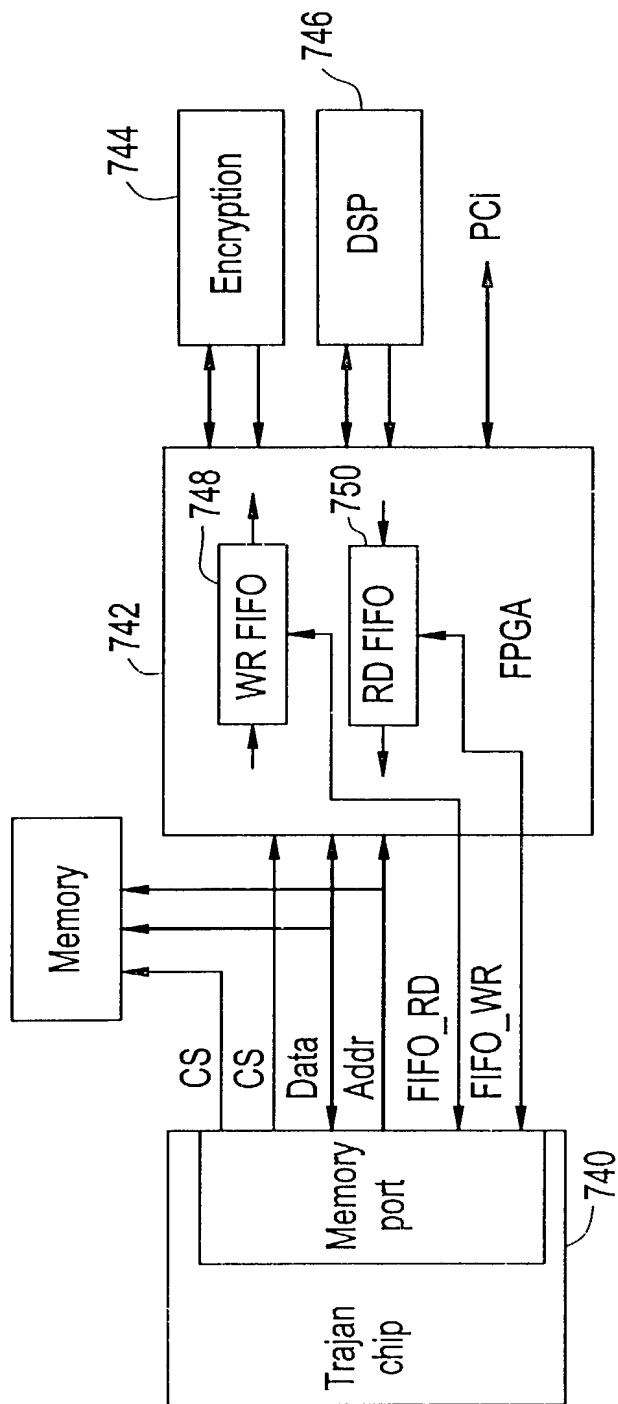
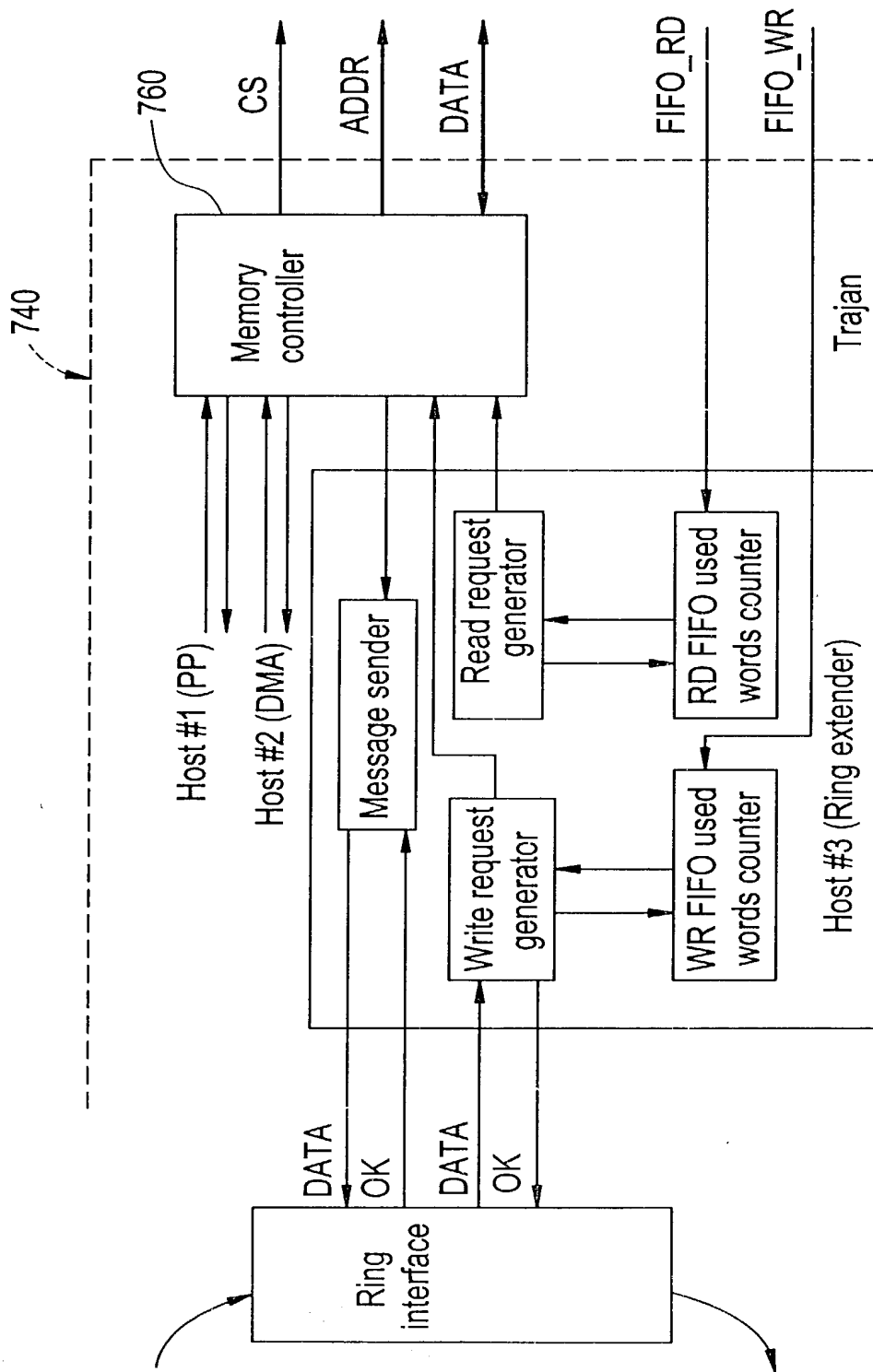
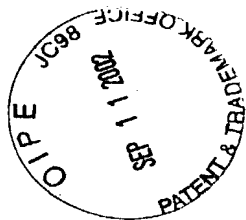


FIG. 66





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FIG. 67

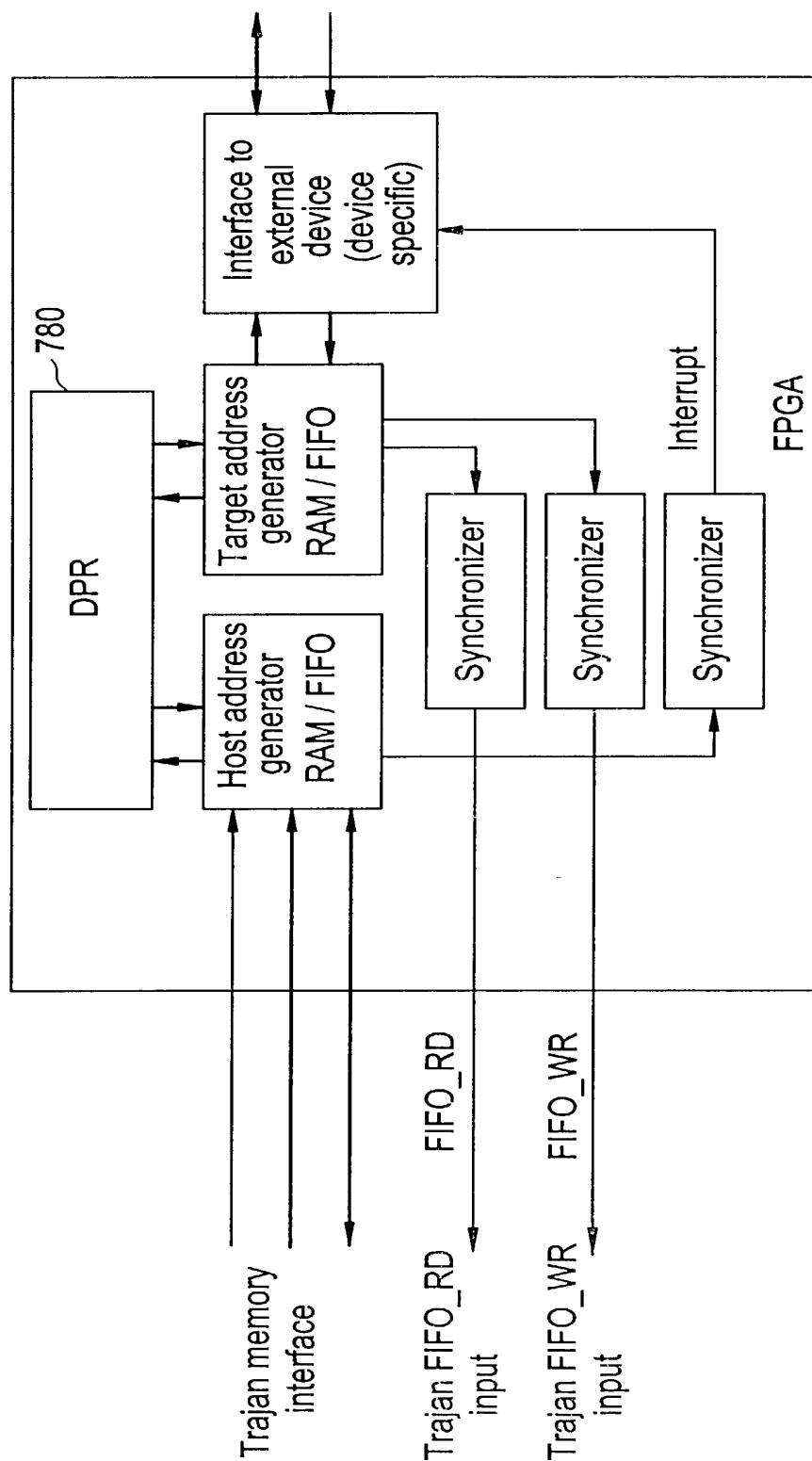
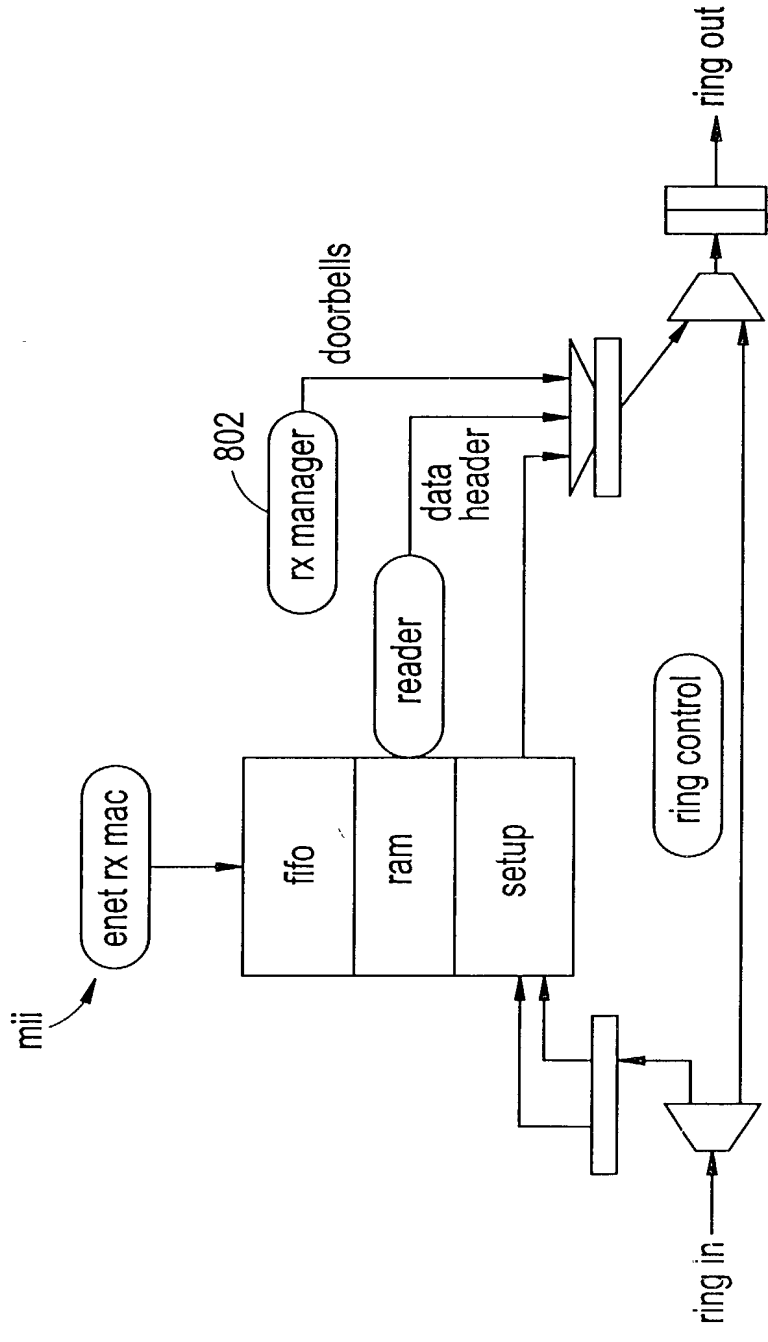




FIG. 68



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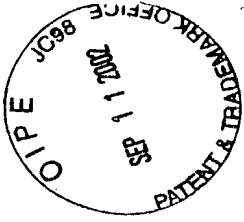
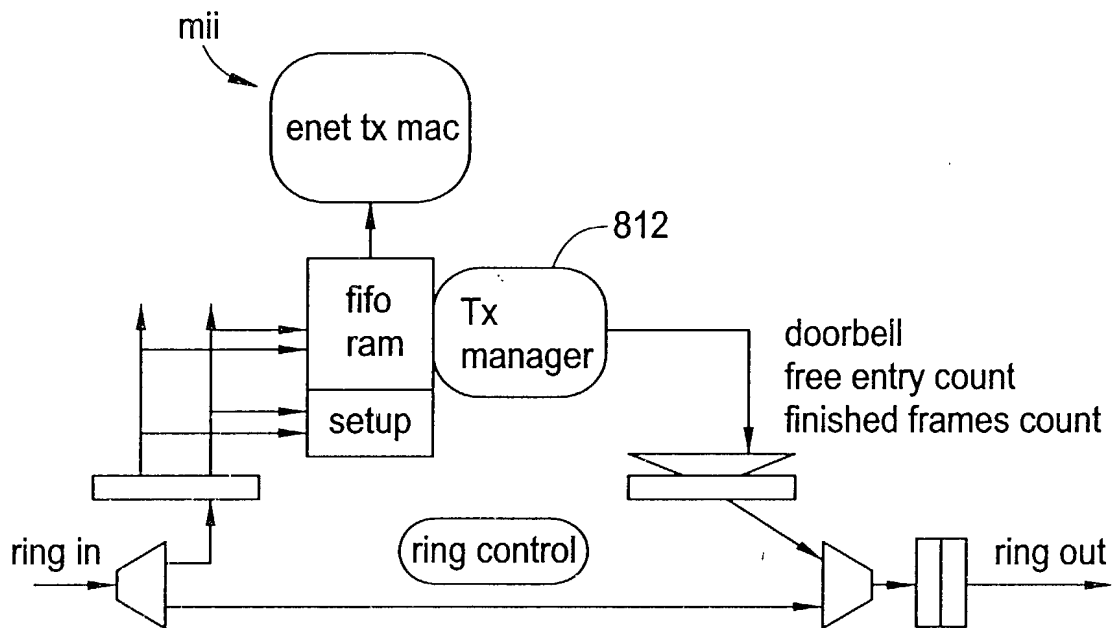


FIG. 69



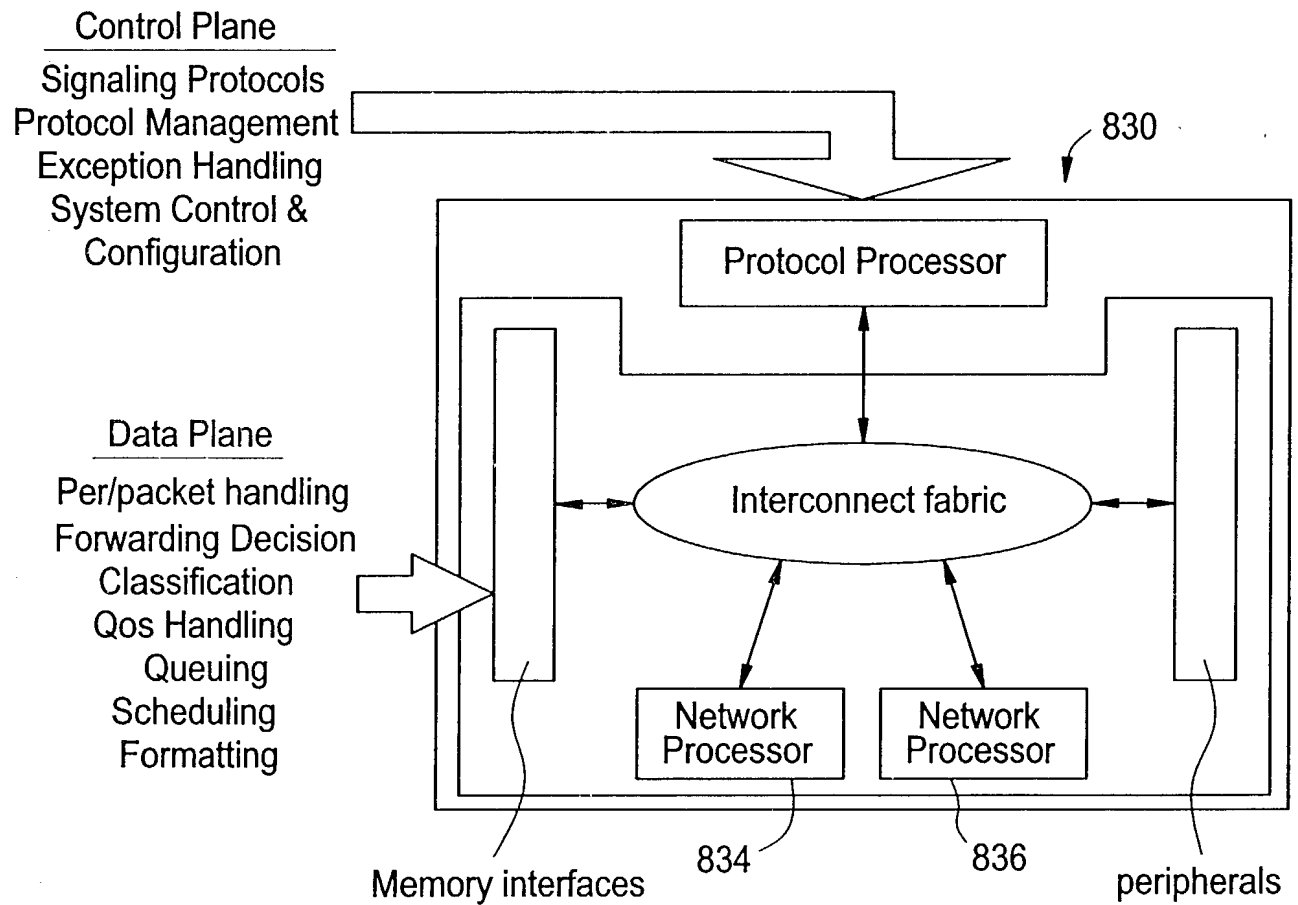
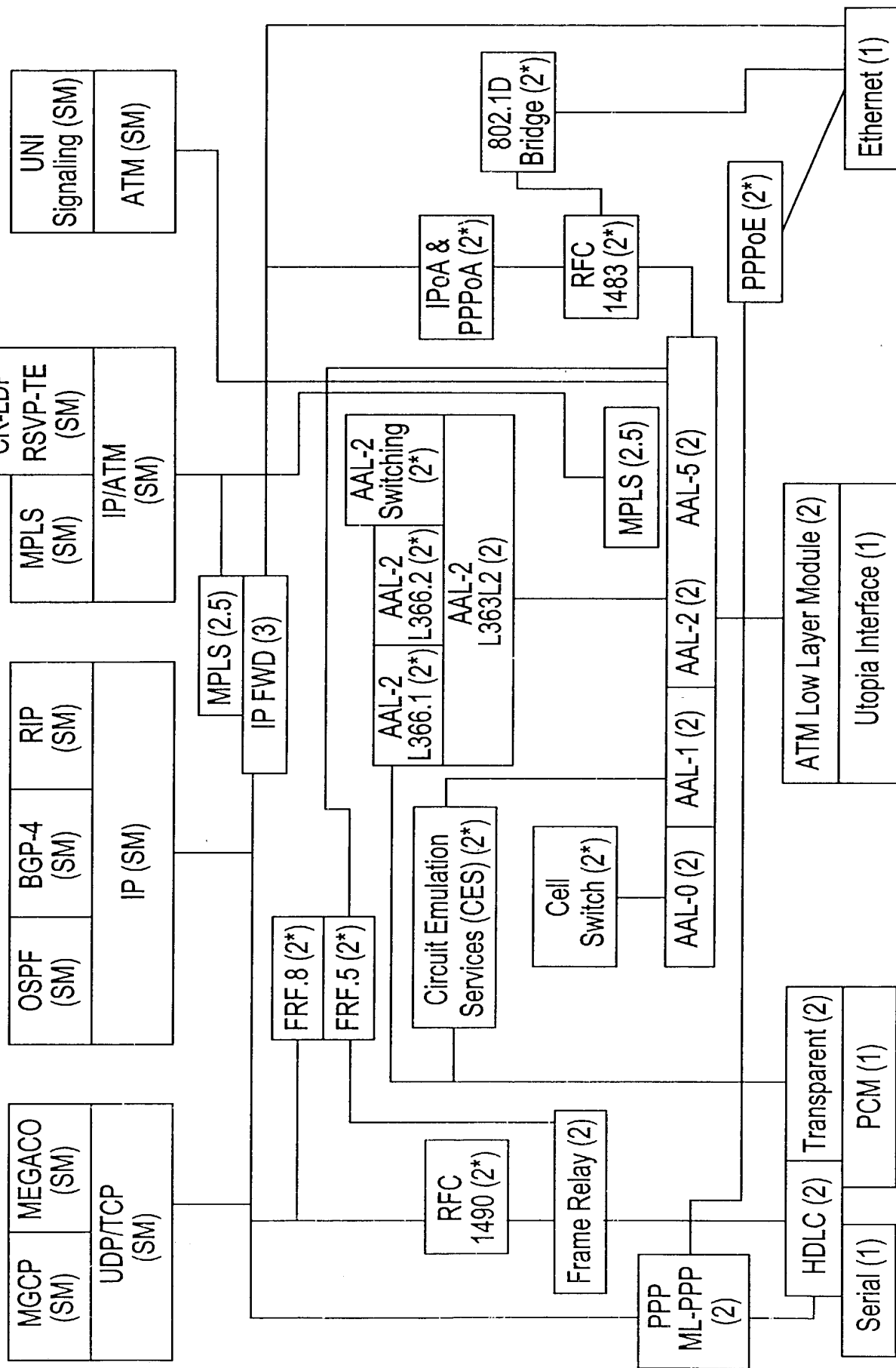
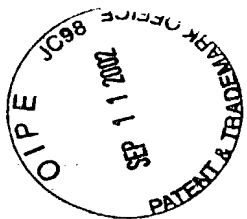


FIG. 71



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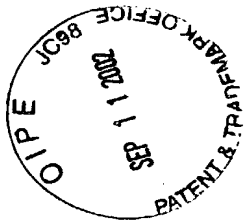
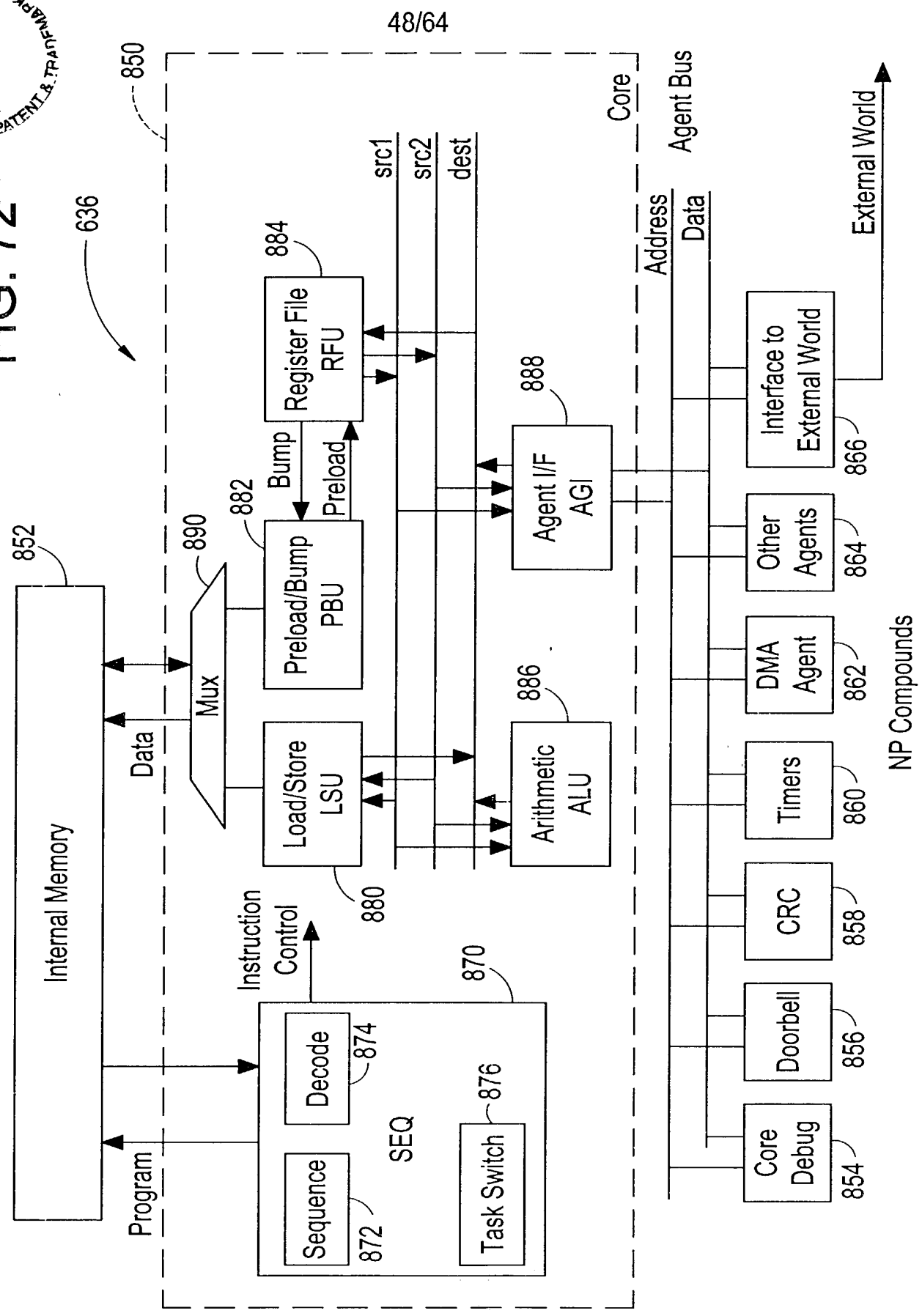


FIG. 72



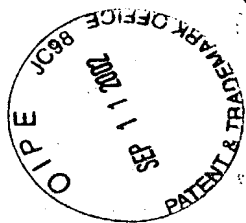
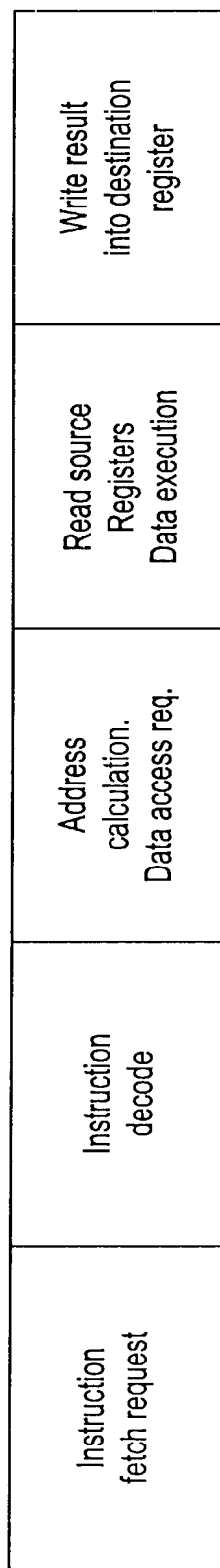
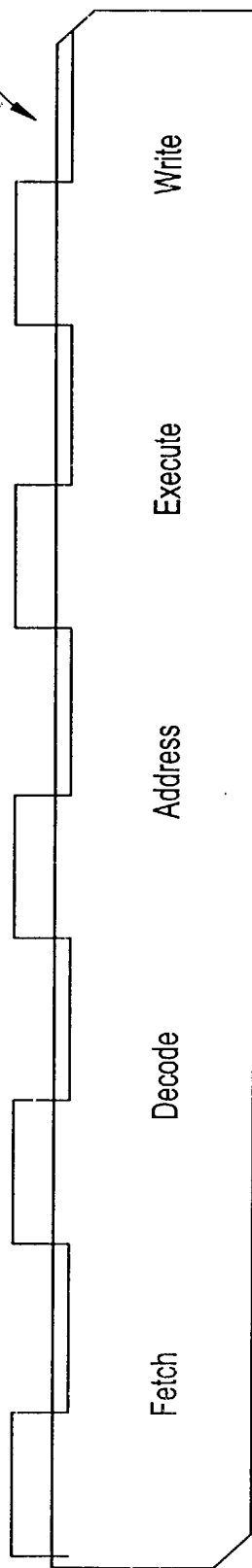
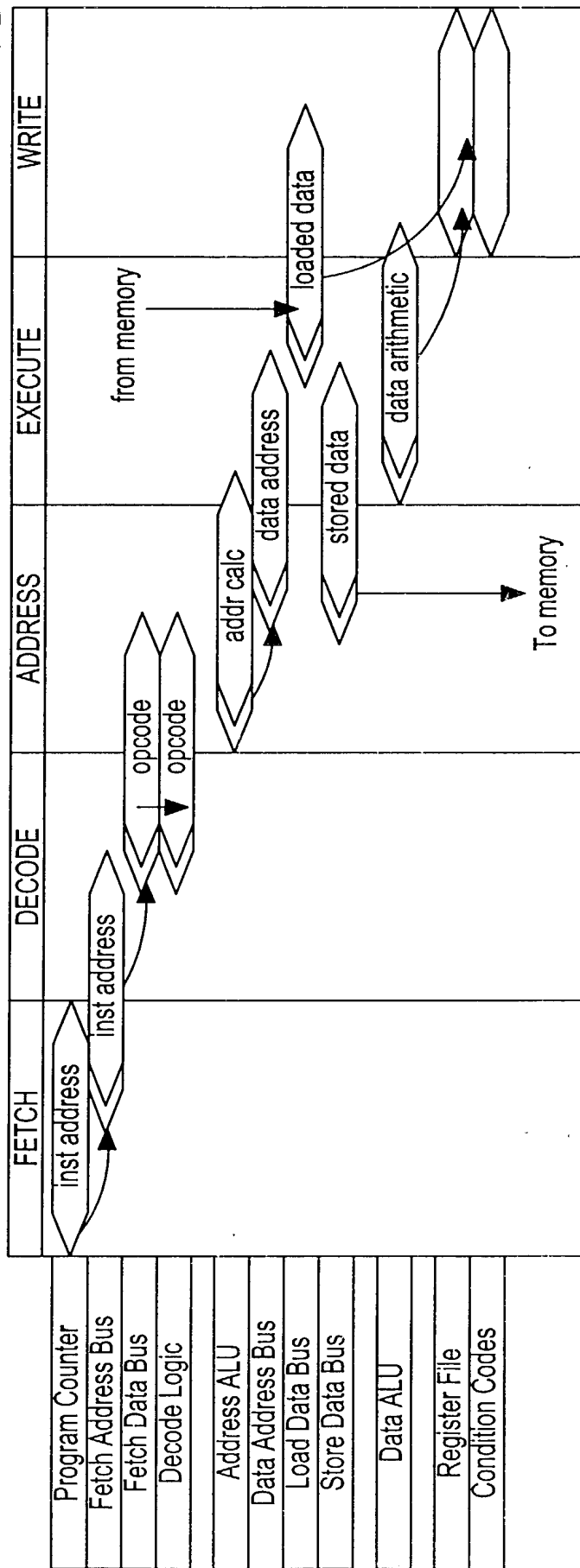


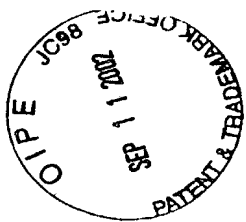
FIG. 73

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FIG. 74

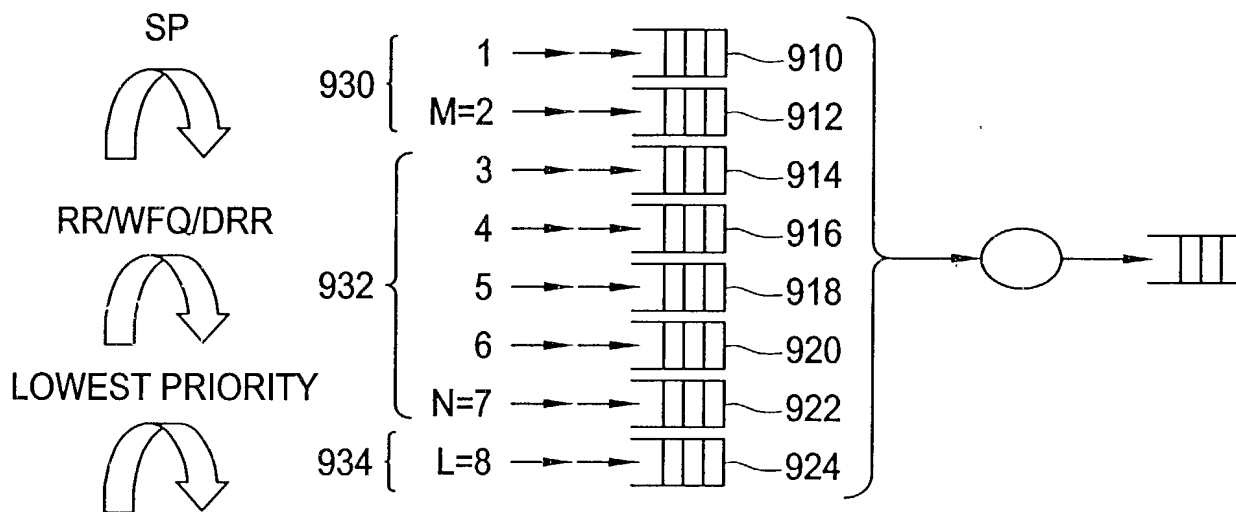
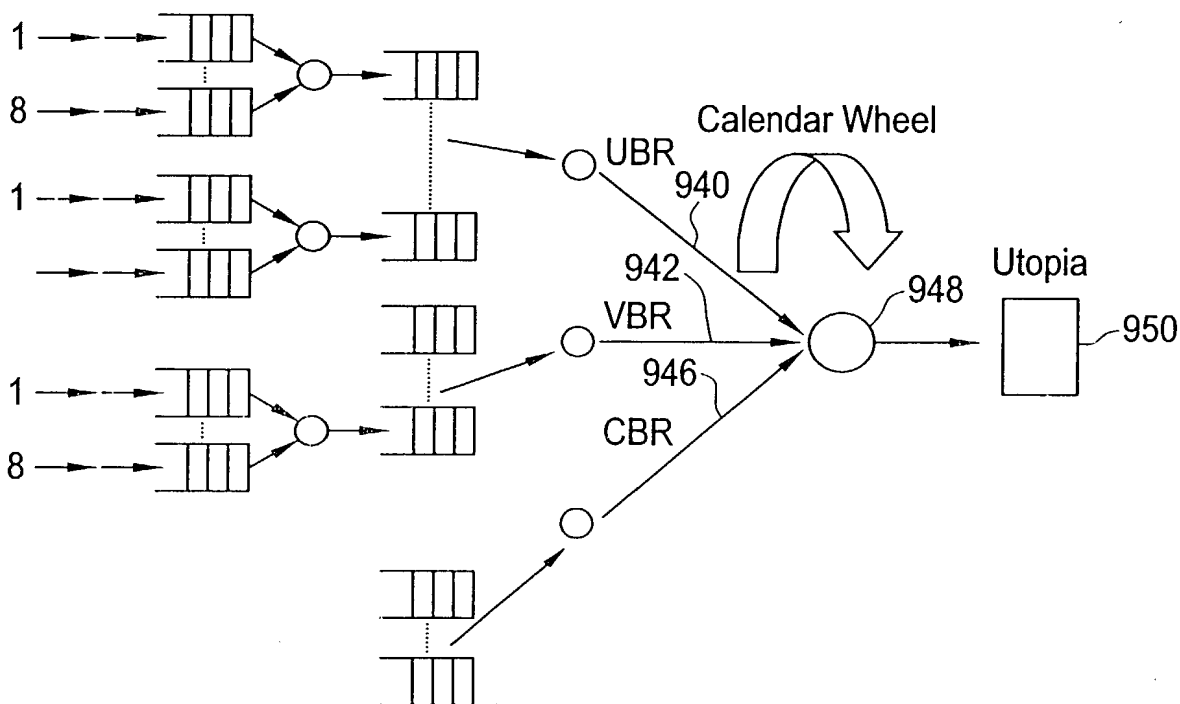


FIG. 75



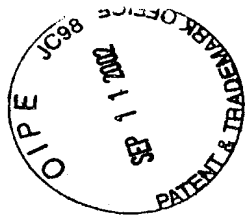


FIG. 76

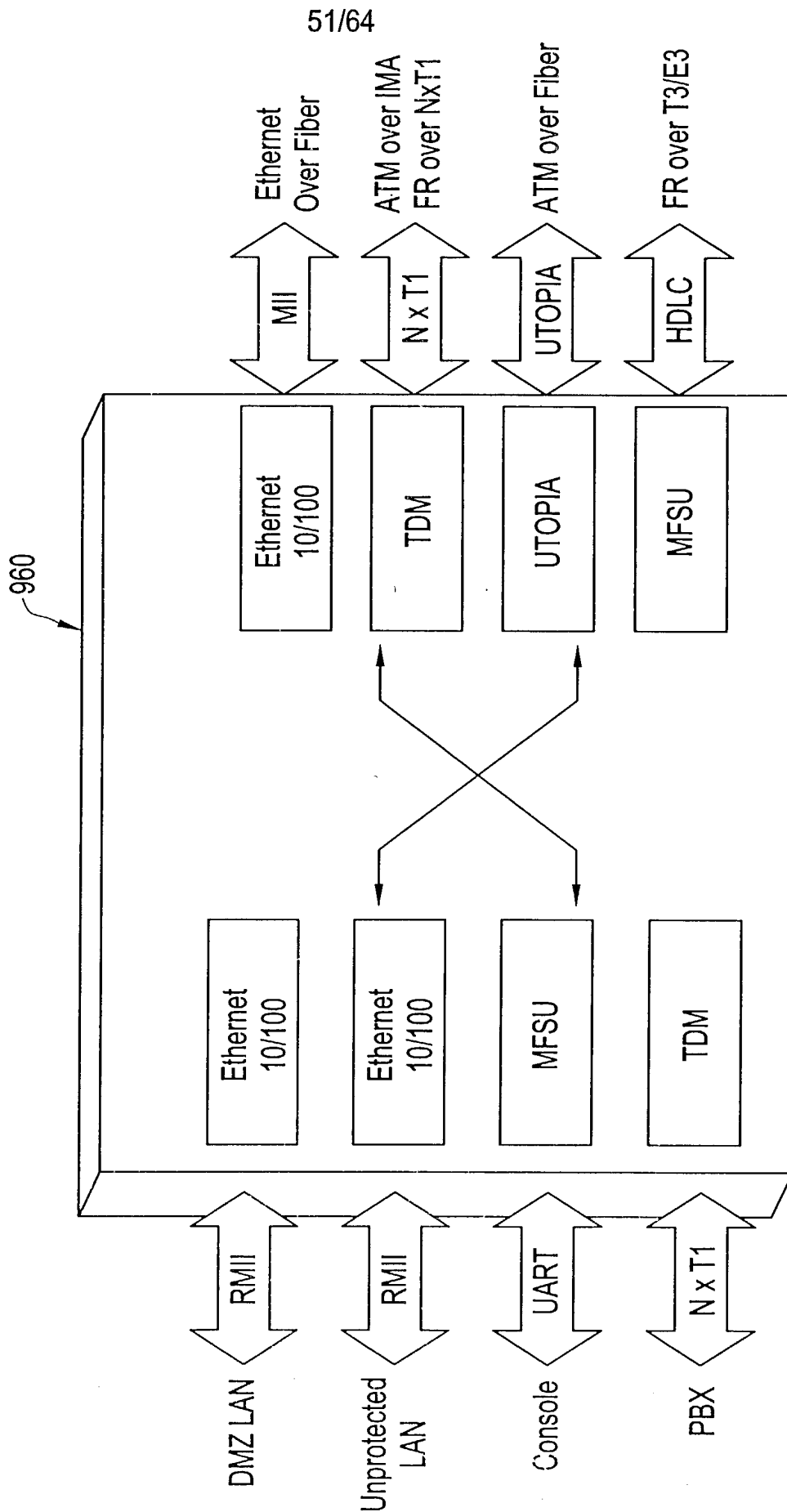
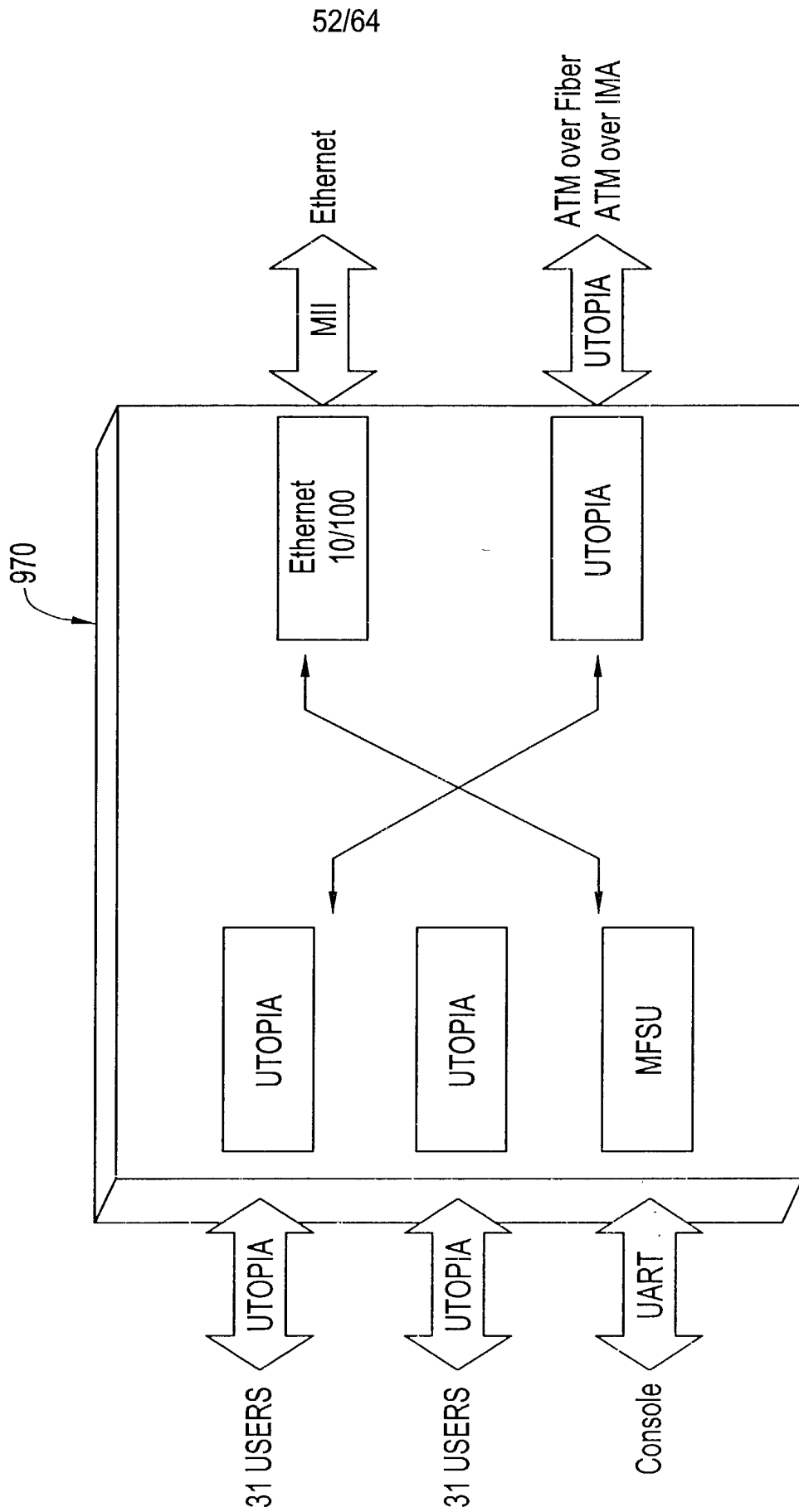


FIG. 77



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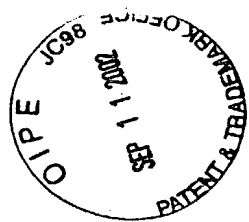
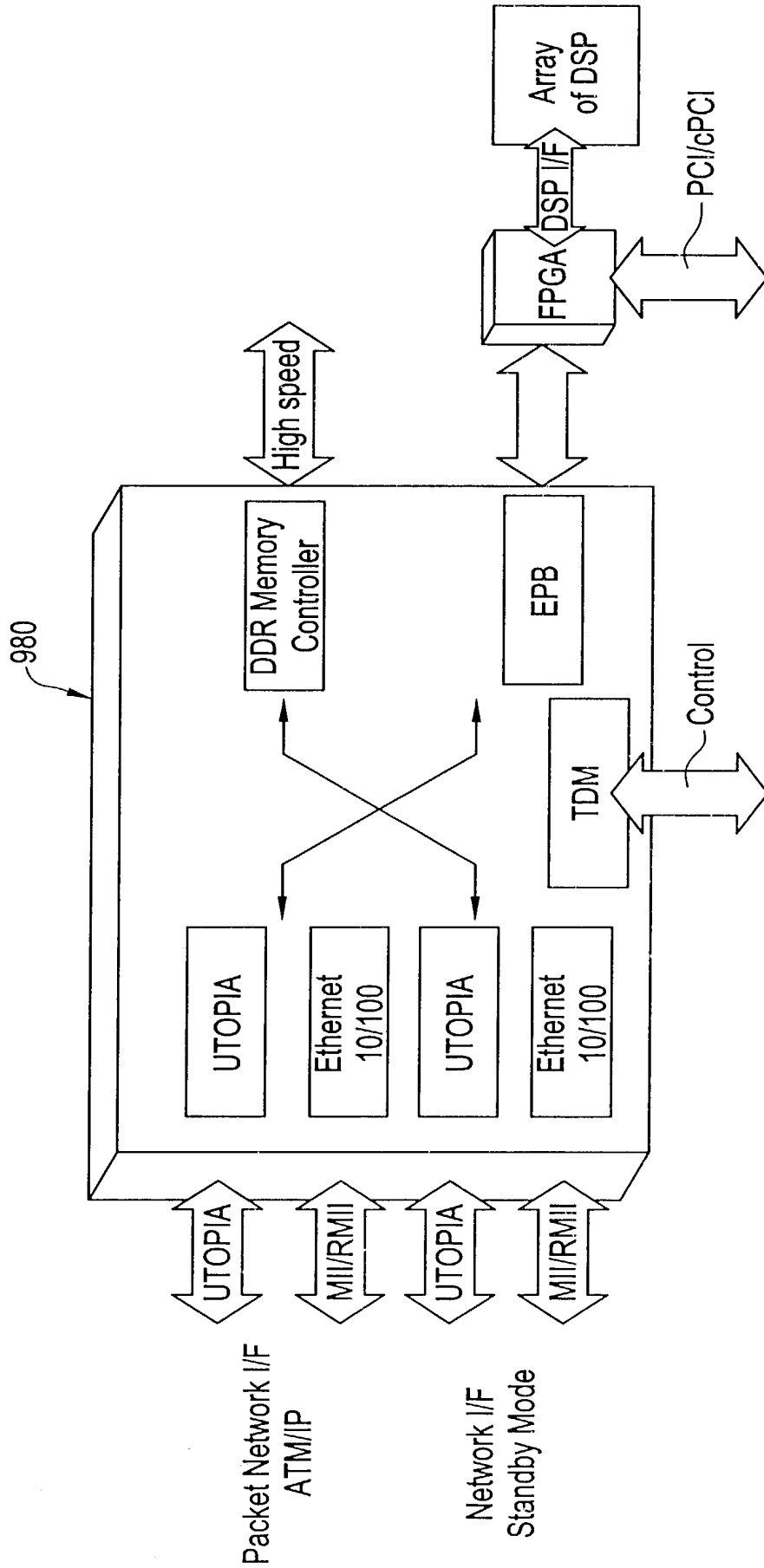


FIG. 78



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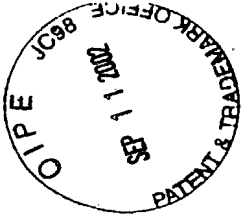
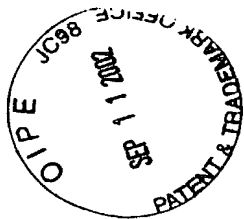


FIG. 78



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FIG. 79

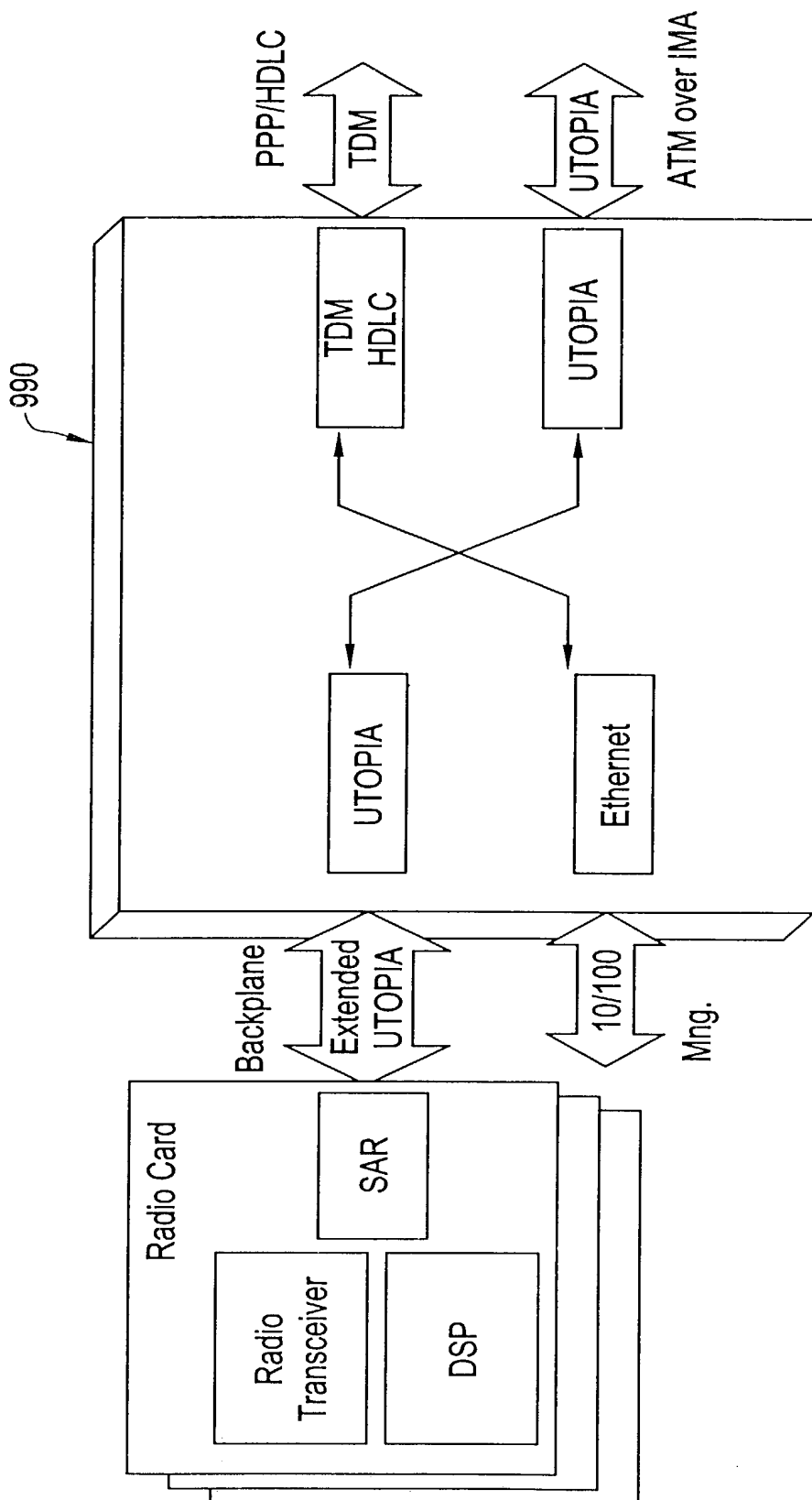
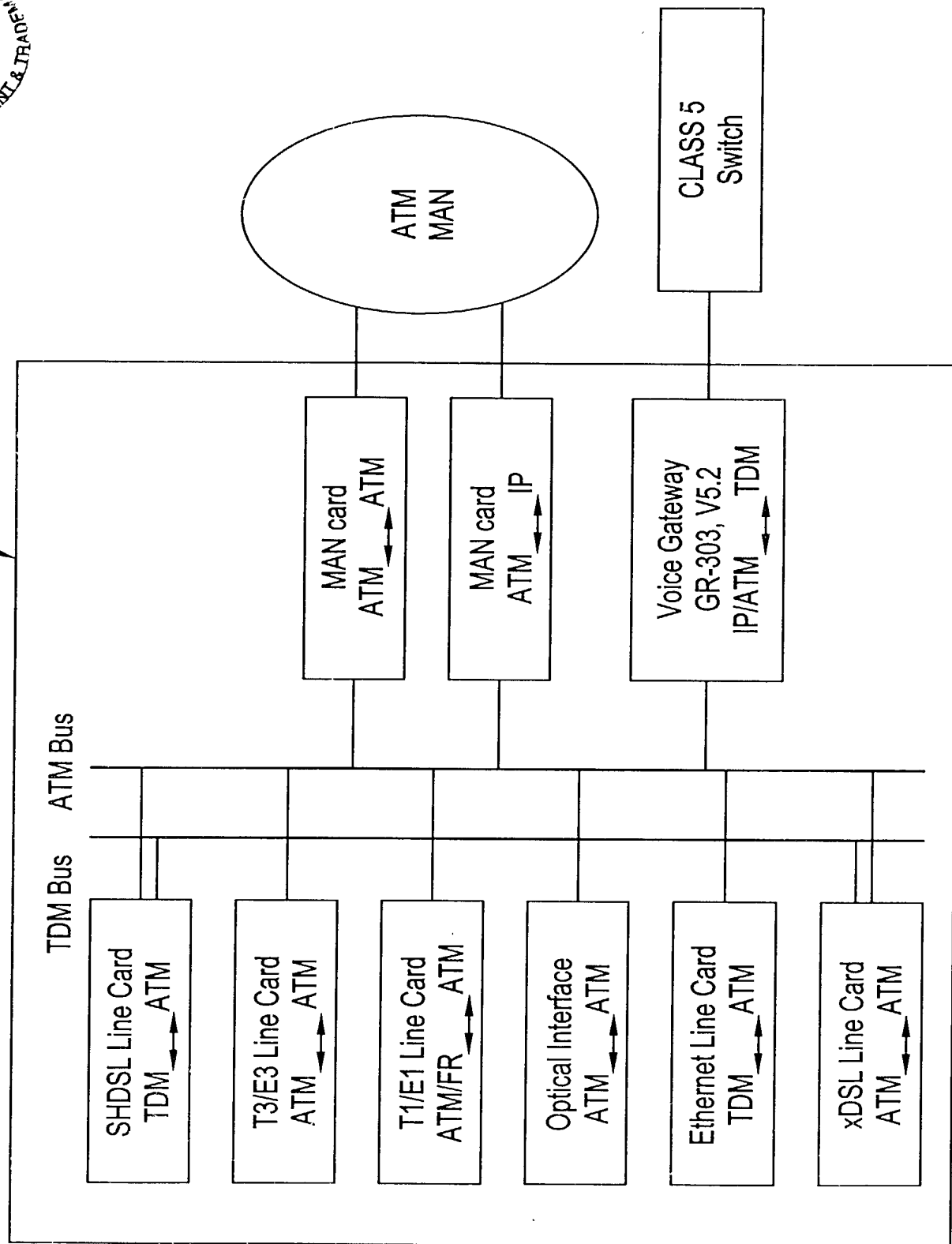
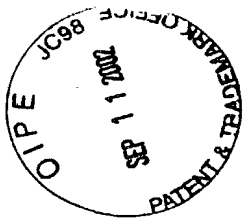


FIG. 79 is a block diagram of a system architecture.

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FIG. 81

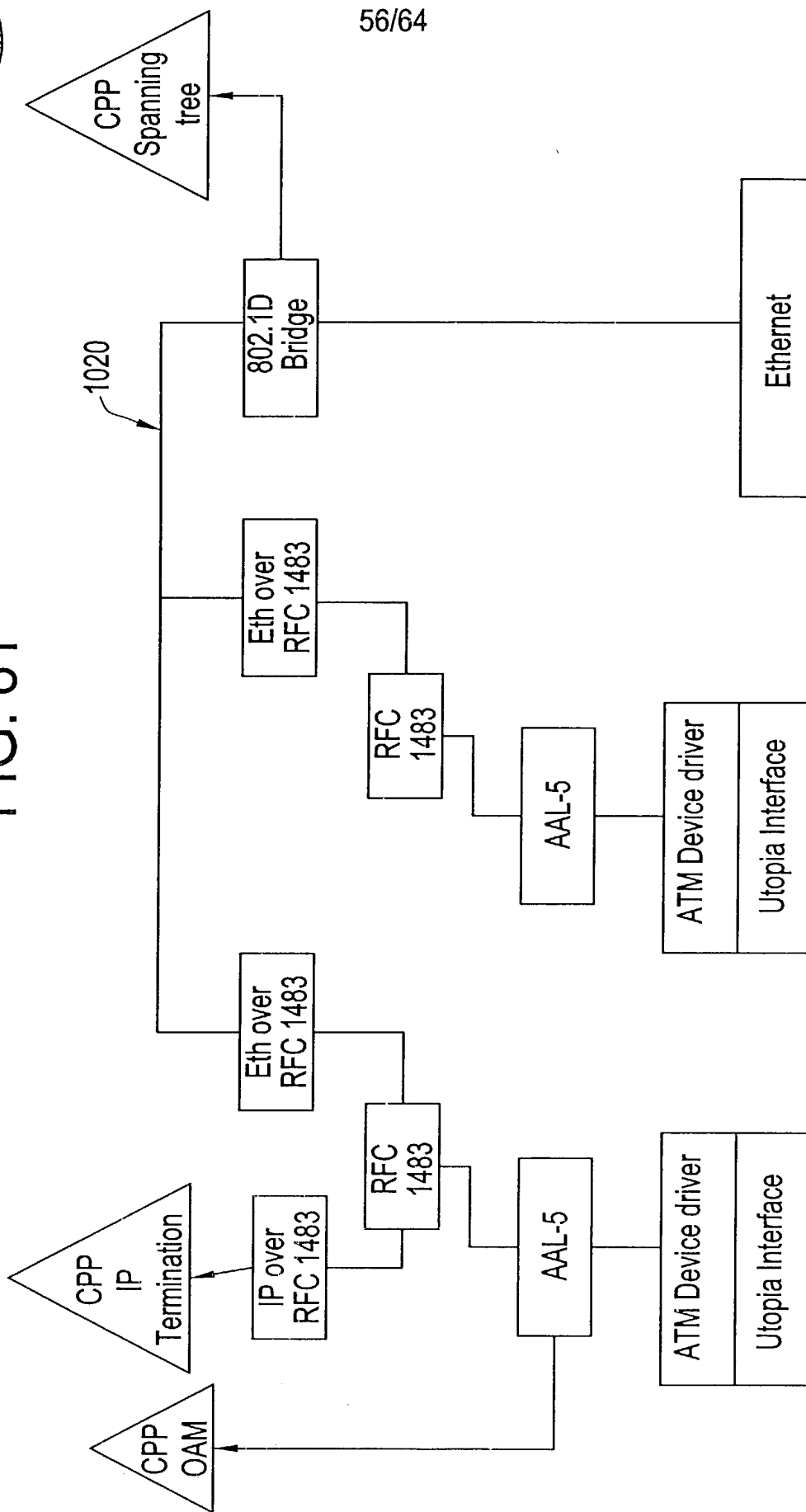
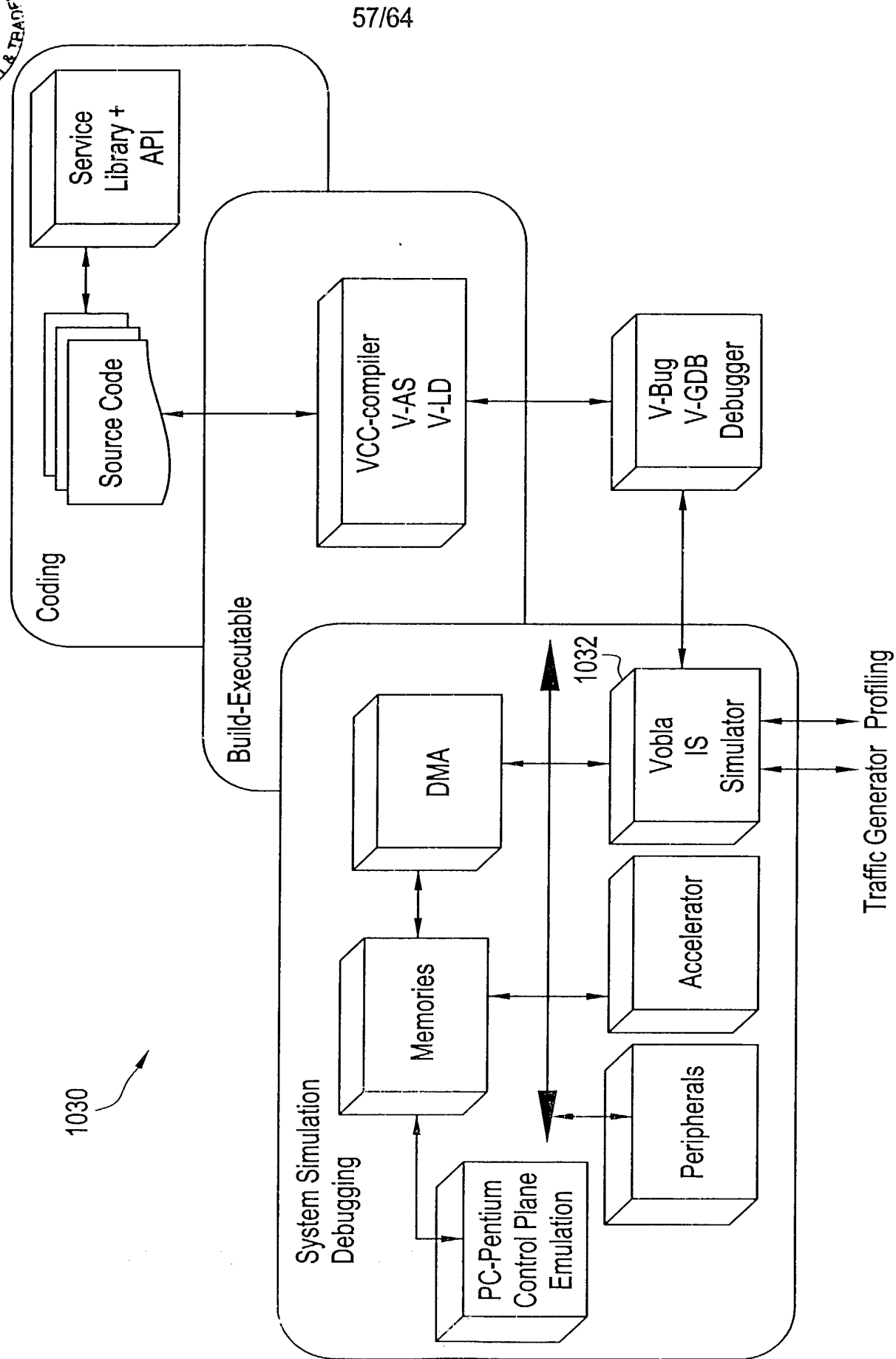
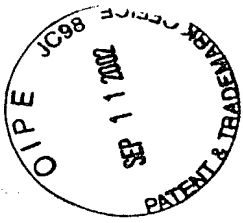


FIG. 82

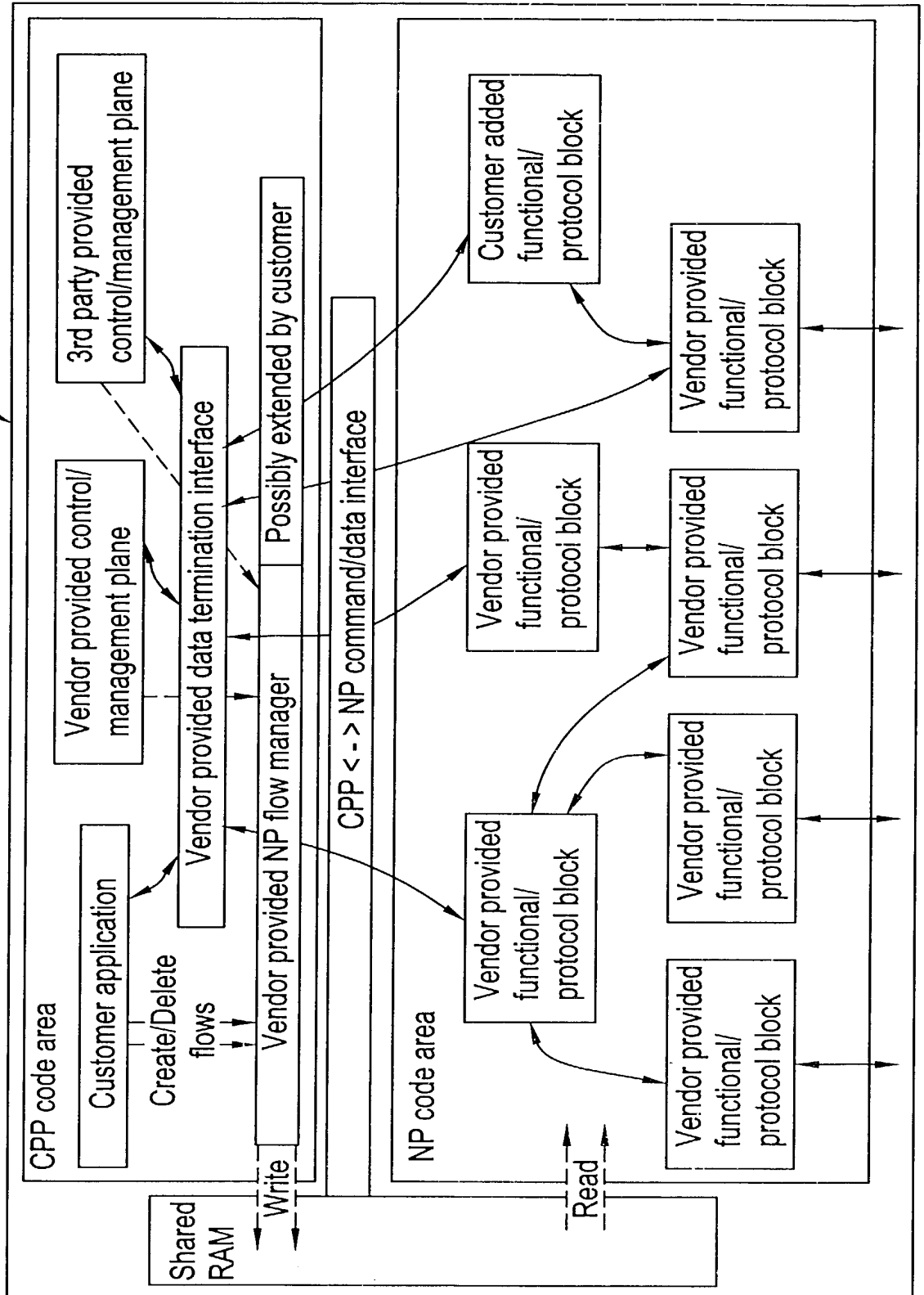




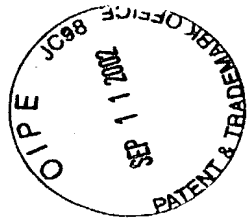
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FIG. 83

1040



[illegible]



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FIG. 85

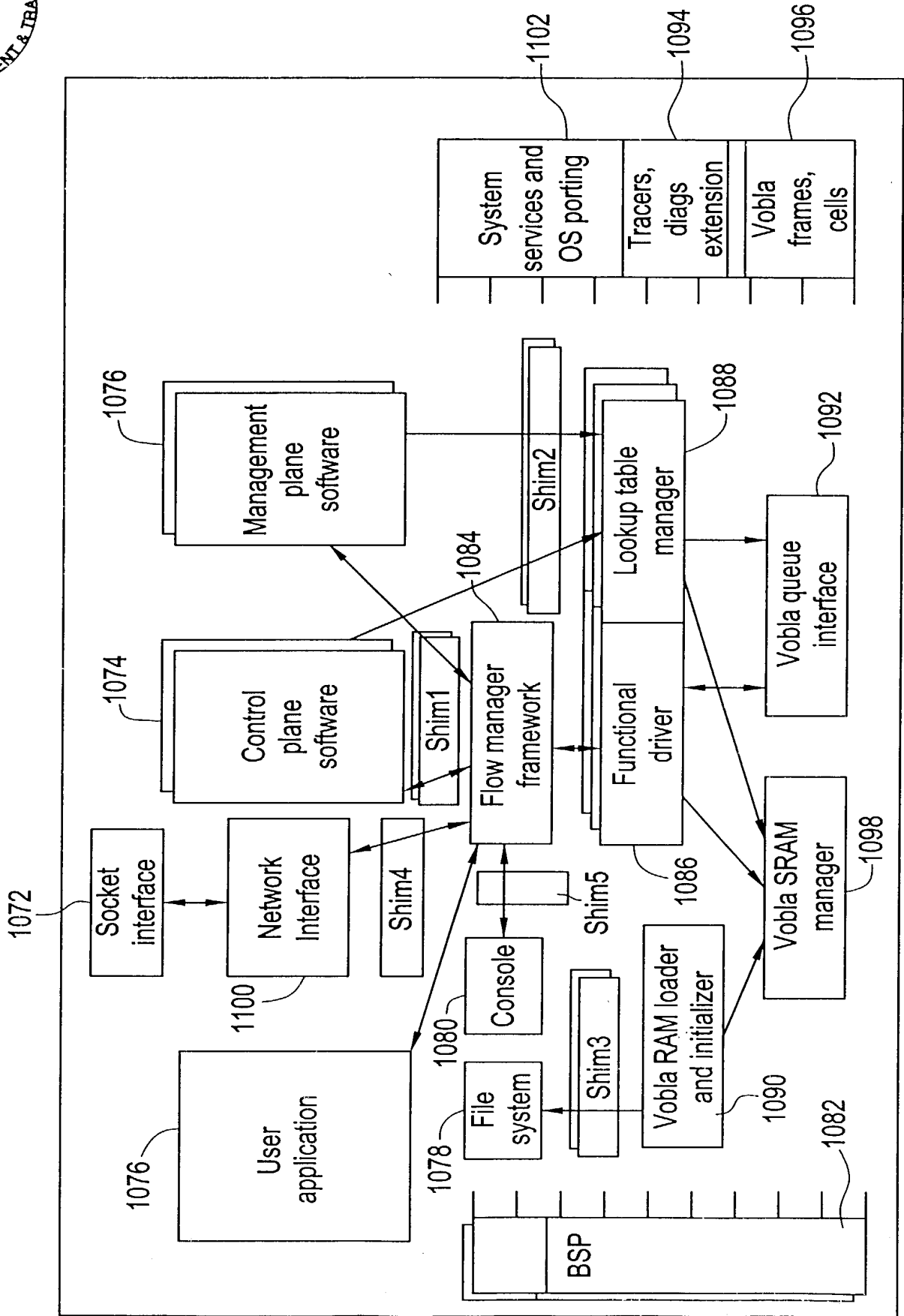
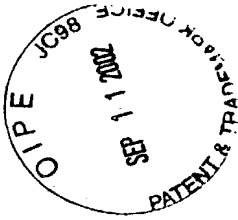
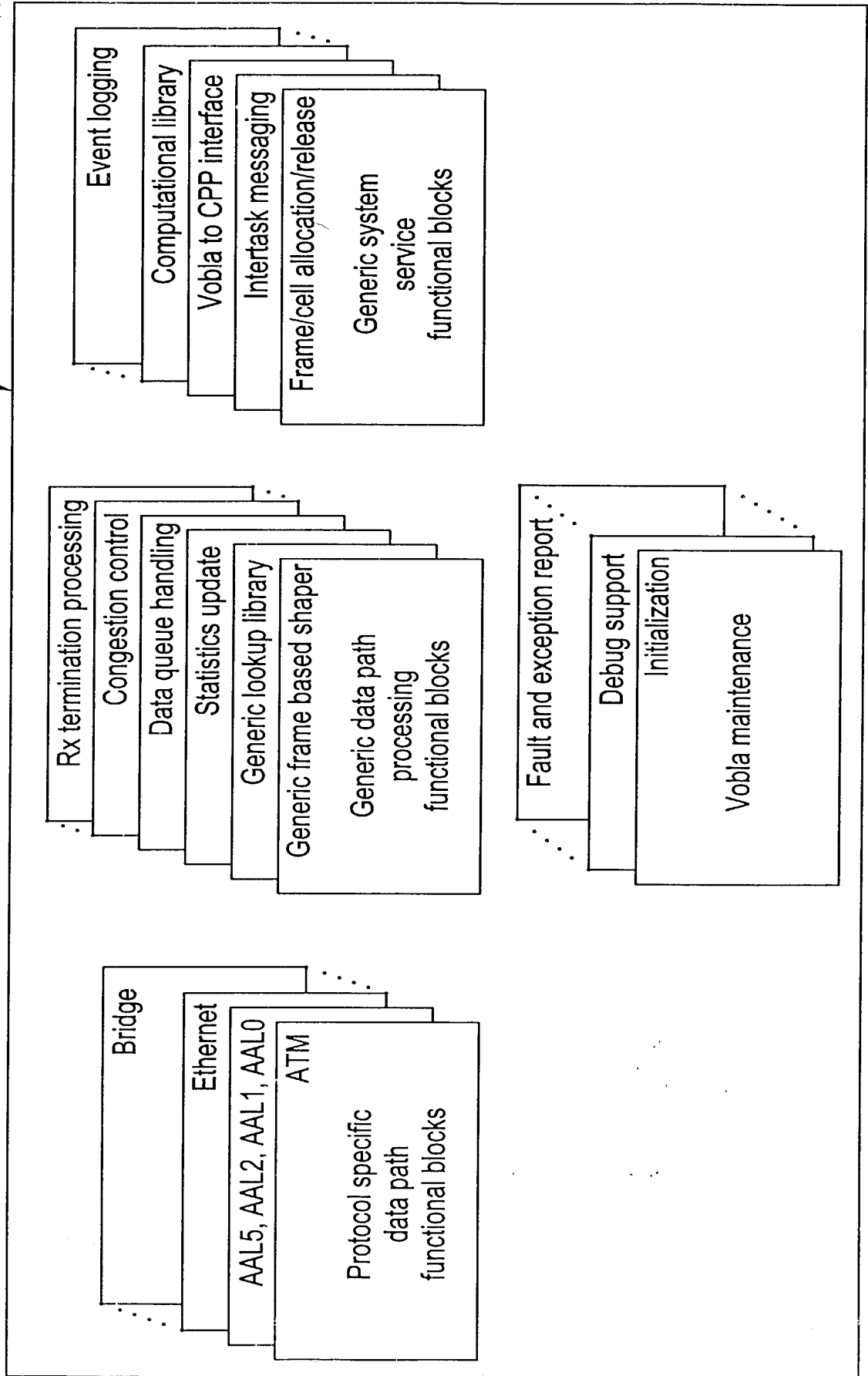
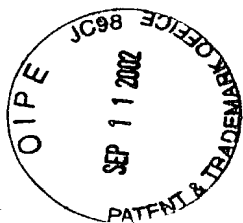


FIG. 86



1200

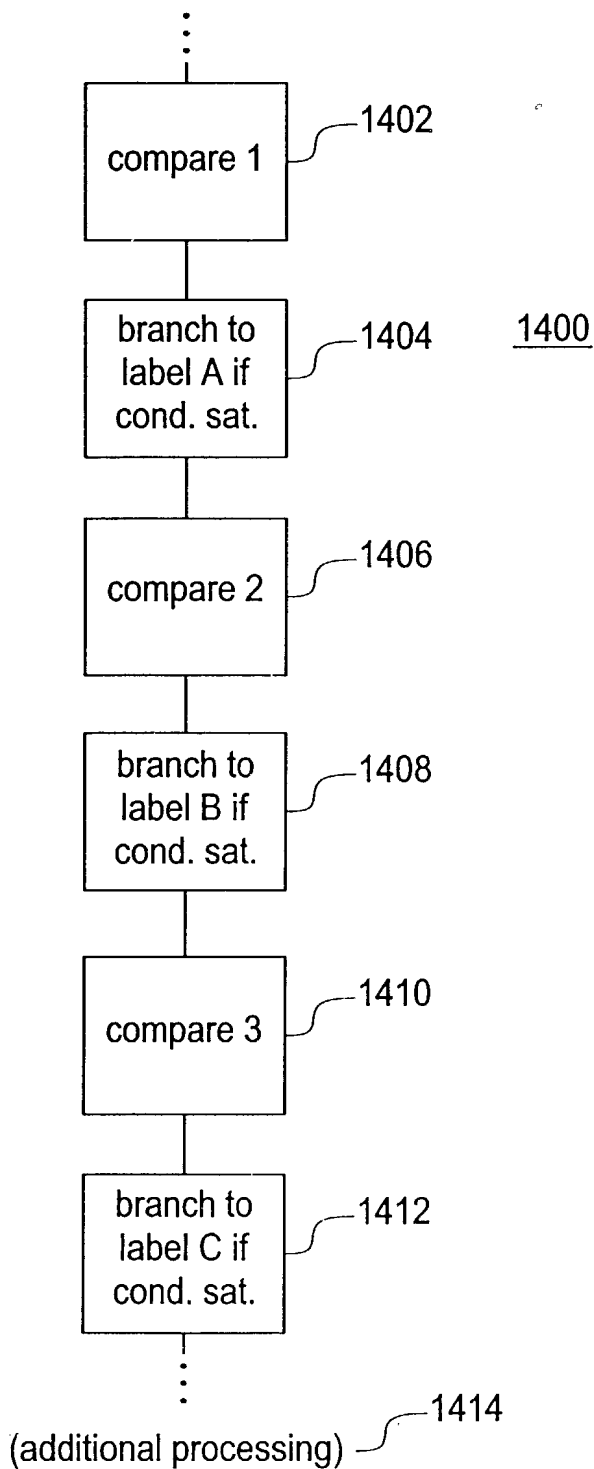


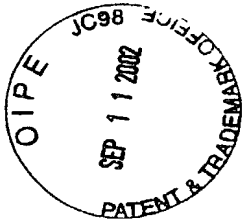


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FIG. 87

PRIOR ART





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FIG. 88

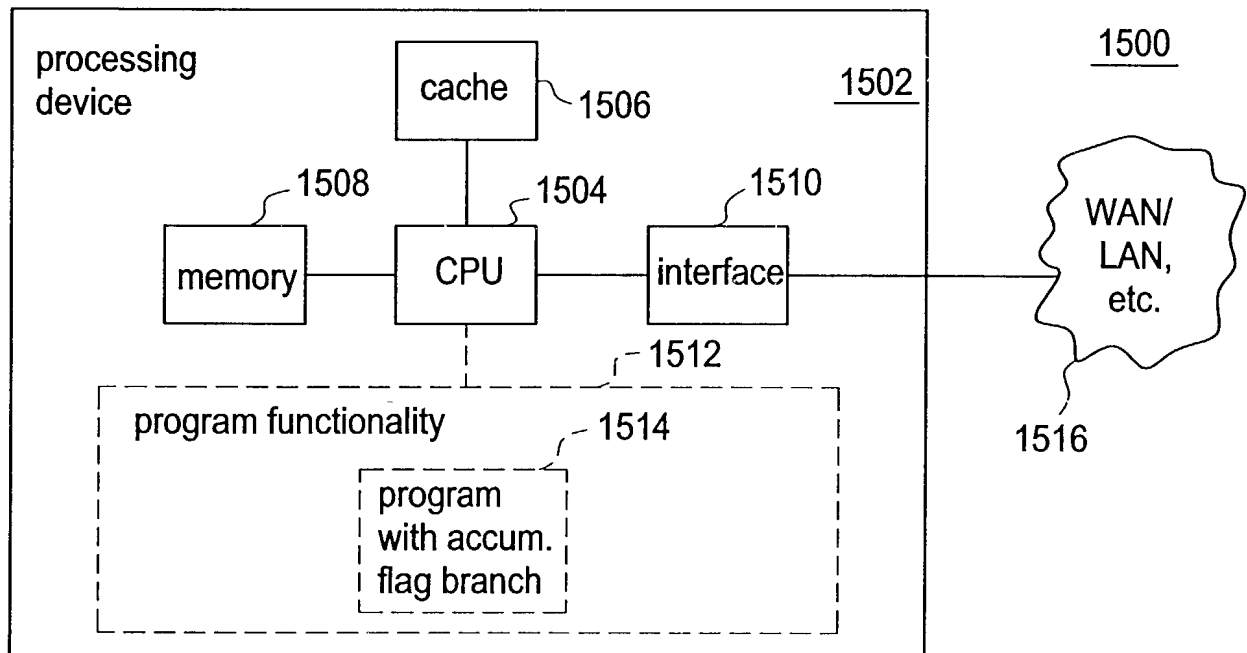
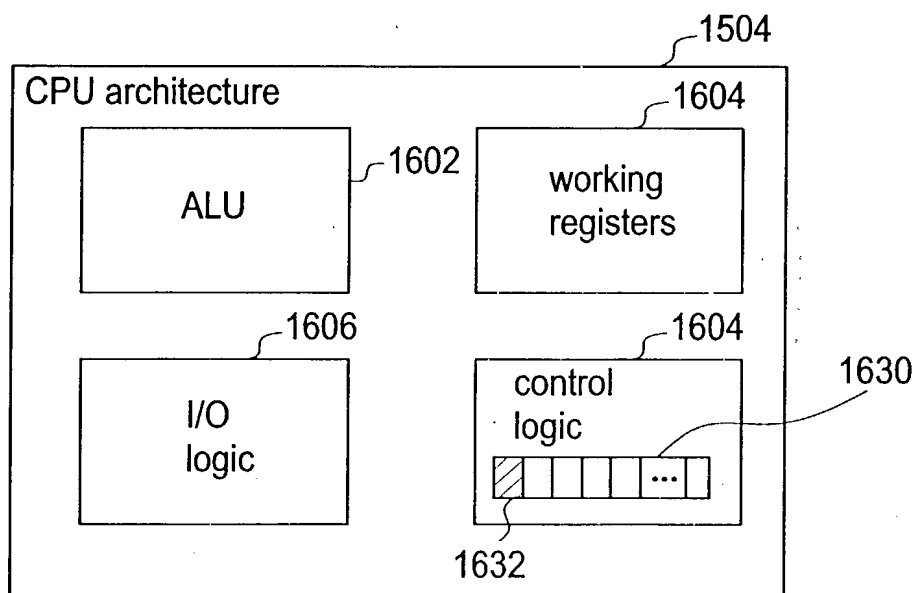
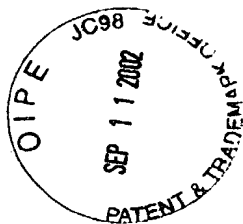


FIG. 89





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FIG. 90

